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UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 03401.P001D3

Total Pages 3

First Named Inventor or Application Identifier Igor Y. Khandros

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ADDRESS TO: Assistant Commissioner for Patents
Box Patent Application
Washington, D. C. 20231

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

1. X Fee Transmittal Form
(Submit an original, and a duplicate for fee processing)
2. X Specification (Total Pages 50)
(preferred arrangement set forth below)
 - Descriptive Title of the Invention
 - Cross References to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to Microfiche Appendix
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claims
 - Abstract of the Disclosure
3. X Drawings(s) (35 USC 113) (Total Sheets 7)
4. X Oath or Declaration (Total Pages 1)
 - a. Newly Executed (Original or Copy)
 - b. X Copy from a Prior Application (37 CFR 1.63(d))
(for Continuation/Divisional with Box 17 completed) (**Note Box 5 below**)
 - i. DELETIONS OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
5. Incorporation By Reference (useable if Box 4b is checked)
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6. Microfiche Computer Program (Appendix)
7. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)
 - a. Computer Readable Copy
 - b. Paper Copy (identical to computer copy)
 - c. Statement verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

8. ☐ Assignment Papers (cover sheet & documents(s))
9. ☐ a. 37 CFR 3.73(b) Statement (where there is an assignee)
- ☐ b. Power of Attorney
10. ☐ English Translation Document (if applicable)
11. ☒ a. Information Disclosure Statement (IDS)/PTO-1449
- ☐ b. Copies of IDS Citations
12. ☒ Preliminary Amendment
13. ☒ Return Receipt Postcard (MPEP 503) (Should be specifically itemized)
14. ☐ a. Small Entity Statement(s)
- b. Statement filed in prior application, Status still proper and desired
15. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)
16. ☒ Other: Copy of postcard with Express Mail Certificate of Mailing
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17. If a **CONTINUING APPLICATION**, check appropriate box and supply the requisite information:

☒ Continuation ☐ Divisional ☐ Continuation-in-part (CIP)

of prior application No: 08/457,479 filed 6-1-95

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Igor Y. Khandros

Application No.:

Filing Date: February 5, 1999

For: METHOD FOR MANUFACTURING
RAISED ELECTRICAL CONTACT
PATTERN OF CONTROLLED
GEOMETRY

Asst. Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to examination of this application, Applicant respectfully requests that the Examiner enter the following amendment.

IN THE SPECIFICATION

On page 1, line 1, please insert --This patent application is a continuation of U.S. Patent Application No. 08/457,479 filed June 1, 1995 (pending) which is a divisional application of U.S. Patent Application No. 08/152,812 filed November 16, 1993 (issued).--

IN THE ABSTRACT

An Abstract is enclosed on a separate page submitted herewith for consideration.

IN THE CLAIMS

Please cancel claims 2-38 before calculating the filing fee in the present application.
Please add the following new claims:

1 39. (New) An electronic assembly comprising:
2 a semiconductor die having a plurality of terminals; and
3 a plurality of interconnection elements, each interconnection element having a
4 portion attached to a respective one of the terminals, and a resilient, elongate, free standing
5 section extending from the portion.

1 40. (New) The electronic assembly of claim 39 wherein each interconnection element
2 comprises:
3 an elongate element, of a first material, attached to a respective terminal of the
4 semiconductor die; and
5 a second material deposited on the flexible elongate precursor element, wherein the
6 elongate element without the second material is flexible, and the elongate element and the
7 second material together are resilient.

1 41. (New) The electronic assembly of claim 40 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum, copper, silver and
3 platinum.

1 42. (New) The electronic assembly of claim 40 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum and copper.

1 43. (New) The electronic assembly of claim 40 wherein the elongate element has a
2 cross-dimension of between 0.0005 and 0.005 inches.

1 44. (New) The electronic assembly of claim 40 wherein elongate element has a cross-
2 dimension of between 0.0007 and 0.003 inches.

1 45. (New) The electronic assembly of claim 40 wherein the second material is attached
2 to the respective terminal.

1 46. (New) The electronic assembly of claim 40 wherein the second material dominates
2 the resiliency of the interconnection element.

1 47. (New) The electronic assembly of claim 40 wherein the second material is stronger
2 than the flexible elongate precursor element.

1 48. (New) The electronic assembly of claim 40 wherein the second material is a coating
2 which envelops the elongate element.

1 49. (New) The electronic assembly of claim 40 wherein the second material includes a
2 material selected from the group consisting of nickel, cobalt, iron, copper, gold, platinum,
3 silver, rhodium and ruthenium.

1 50. (New) The electronic assembly of claim 40 wherein the second material includes a
2 material selected from the group consisting of nickel, cobalt and iron.

1 51. (New) The electronic assembly of claim 40 wherein the second material is between
2 0.00005 and 0.007 inches thick.

1 52. (New) The electronic assembly of claim 40 wherein the second material is between
2 0.00010 and 0.003 inches thick.

1 53. (New) The electronic assembly of claim 40 wherein the elongate element has a
2 cross-dimension of between 0.0005 and 0.005 inches and the second material is between
3 0.00005 and 0.007 inches thick.

1 54. (New) The electronic assembly of claim 40 wherein the first material and the
2 second material are both conductive.

1 55. (New) The electronic assembly of claim 54 wherein the second material is
2 deposited directly on the flexible elongate precursor element.

1 56. (New) The electronic assembly of claim 40 further comprising a barrier layer
2 between the flexible elongate precursor element and the second material.

1 57. (New) The electronic assembly of claim 40 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum and copper, and the second
3 material includes a material selected from the group consisting of nickel, cobalt and iron.

1 58. (New) The electronic assembly of claim 40 wherein the elongate element is a core
2 element and the second material is a coating which is deposited around the core element.

1 59. (New) The electronic assembly of claim 39 wherein each interconnection element
2 changes direction at least once.

1 60. (New) The electronic assembly of claim 59 wherein the interconnection element
2 extends from the semiconductor die, whereafter the interconnection element changes
3 direction, whereafter the interconnection element at least partially returns in direction away
4 from the semiconductor die.

1 61. (New) The electronic assembly of claim 39 wherein the interconnection element has
2 a contact region, distant from the semiconductor die, which remains distant from the
3 semiconductor die upon depression of the contact region towards the semiconductor die.

4

1 62. (New) The electronic assembly of claim 39 wherein, for each interconnection
2 element of a first plurality of the interconnection elements, a contact region distant from the
3 semiconductor die on a given interconnection element is substantially in a common plane
4 with corresponding contact regions of the first plurality of interconnection elements.

1 63. (New) An electronic assembly comprising:
2 a substrate having a plurality of terminals; and
3 a plurality of free standing interconnection elements, each including:
4 an elongate element, of a first material, having a portion connected to a
5 respective terminal of the substrate; and
6 a second material, on the elongate element, wherein the elongate element is
7 flexible without the second material, and the elongate element and the second material
8 together are resilient.

1 64. (New) The electronic assembly of claim 63 wherein the substrate comprises a
2 material selected from the group consisting of a semiconductor die, a printed circuit board,
3 a plastic substrate, a ceramic substrate, and a teflon based substrate.

1 65. (New) The electronic assembly of claim 63 wherein the first material is a readily
2 shaped-material and the second material provides resiliency to the free standing
3 interconnection element.

1 66. (New) The electronic assembly of claim 63 wherein each interconnection element
2 changes direction at least once.

1 67. (New) The electronic assembly of claim 63 wherein, for each interconnection
2 element of a first plurality of the free standing interconnection elements, a contact region
3 distant from the substrate on a given interconnection element is substantially in a common
4 plane with corresponding contact regions of the first plurality of interconnection elements.

1 68. (New) The electronic assembly of claim 63 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum, copper, silver and
3 platinum.

1 69. (New) The electronic assembly of claim 63 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum and copper.

1 70. (New) The electronic assembly of claim 63 wherein the elongate element has a
2 cross-dimension of between 0.0005 and 0.005 inches.

1 71. (New) The electronic assembly of claim 63 wherein the elongate element has a
2 cross-dimension of between 0.0007 and 0.003 inches.

1 72. (New) The electronic assembly of claim 63 wherein the second material is
2 connected to the respective terminal.

1 73. (New) The electronic assembly of claim 63 wherein the second material dominates
2 the resiliency of the interconnection element.

1 74. (New) The electronic assembly of claim 63 wherein the second material is stronger
2 than the elongate element.

1 75. (New) The electronic assembly of claim 63 wherein the second material is a coating
2 which envelopes the elongate element.

1 76. (New) The electronic assembly of claim 63 wherein the second material includes a
2 material selected from the group consisting of nickel, cobalt, iron, copper, gold, platinum,
3 silver, rhodium and ruthenium.

1 77. (New) The electronic assembly of claim 63 wherein the second material includes a
2 material selected from the group consisting of nickel, cobalt and iron.

1 78. (New) The electronic assembly of claim 63 wherein the second material is between
2 0.00005 and 0.007 inches thick.

1 79. (New) The electronic assembly of claim 63 wherein the second material is between
2 0.00010 and 0.003 inches thick.

1 80. (New) The electronic assembly of claim 63 wherein the elongate element has a
2 cross-dimension of between 0.0005 and 0.005 inches and the second material is between
3 0.00005 and 0.007 inches thick.

1 81. (New) The electronic assembly of claim 63 wherein the first material and the
2 second material are both conductive.

1 82. (New) The electronic assembly of claim 81 wherein the second material is formed
2 directly on the elongate element.

1 83. (New) The electronic assembly of claim 63 further comprising a barrier layer
2 between the elongate element and the second material.

1 84. (New) The electronic assembly of claim 63 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum and copper, and the second
3 material includes a material selected from the group consisting of nickel, cobalt and iron.

1 85. (New) The electronic assembly of claim 63 wherein the elongate element is a core
2 element and the second material is a coating which is deposited around the core element.

1 86. (New) An electronic assembly comprising:
2 a substrate having a plurality of terminals; and
3 a plurality of free standing interconnection elements, each including:
4 an elongate core element, of a first material, having an end directly attached
5 to a respective terminal; and
6 a coating, of a second material, which is deposited around the elongate core
7 element, wherein the elongate core element is flexible without the coating, and the elongate
8 core element and the coating together are resilient.

1 87. (New) An electronic assembly of claim 86 wherein the substrate comprises a
2 material selected from the group consisting of a semiconductor die, a printed circuit board,
3 a plastic substrate, a ceramic substrate, and a teflon based substrate.

1 88. (New) The electronic assembly of claim 86 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum and copper.

1 89. (New) The electronic assembly of claim 86 wherein the elongate core element has a
2 cross-dimension of between 0.0007 and 0.003 inches.

1 90. (New) The electronic assembly of claim 86 wherein the second material is attached
2 to the respective terminal.

1 91. (New) The electronic assembly of claim 86 wherein the second material dominates
2 the resiliency of the interconnection element.

1 92. (New) The electronic assembly of claim 86 wherein the second material includes a
2 material selected from the group consisting of nickel, cobalt and iron.

1 93. (New) The electronic assembly of claim 86 wherein the second material is between
2 0.00010 and 0.003 inches thick.

1 94. (New) The electronic assembly of claim 86 wherein the elongate core element has a
2 cross-dimension of between 0.0005 and 0.005 inches and the second material is between
3 0.00005 and 0.007 inches thick.

1 95. (New) The electronic assembly of claim 86 wherein the elongate core element
2 includes a material selected from the group consisting of gold, aluminum and copper, and
3 the second material includes a material selected from the group consisting of nickel, cobalt
4 and iron.

1 96. (New) An electronic assembly comprising:
2 a substrate having a plurality of terminals;
3 a plurality of interconnection elements, each having:
4 an elongate element, of a first material, having an end directly attached to a
5 respective terminal; and
6 a second material on the elongate element, the second material being
7 attached to the terminal, wherein the elongate element is flexible without the second
8 material, and the elongate element and the second material together are resilient.

1 97. (New) The electronic assembly of claim 96 wherein the substrate comprises a
2 material selected from the group consisting of a semiconductor die, a printed circuit board,
3 a plastic substrate, a ceramic substrate, and a teflon based substrate.

1 98. (New) The electronic assembly of claim 96 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum and copper.

1 99. (New) The electronic assembly of claim 96 wherein the elongate element has a
2 cross-dimension of between 0.0007 and 0.003 inches.

1 100. (New) The electronic assembly of claim 96 wherein the second material dominates
2 the resiliency of the interconnection element.

1 101. (New) The electronic assembly of claim 96 wherein the second material includes a
2 material selected from the group consisting of nickel, cobalt and iron.

1 102. (New) The electronic assembly of claim 96 wherein the second material is between
2 0.00010 and 0.003 inches thick.

1 103. (New) The electronic assembly of claim 96 wherein the elongate element has a
2 cross-dimension of between 0.0005 and 0.005 inches and the second material is between
3 0.00005 and 0.007 inches thick.

1 104. (New) The electronic assembly of claim 96 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum and copper, and the coating
3 includes a material selected from the group consisting of nickel, cobalt and iron.

1 105. (New) An electronic assembly comprising:
2 a substrate having a plurality of terminals; and
3 a plurality of interconnection elements, each including:
4 a elongate element, of a first material, having a portion connected to a
5 respective terminal of the substrate; and
6 a second material on the elongate element, wherein the elongate element is
7 flexible without the second material, and the elongate element and the second material
8 together are resilient with the second material dominating the resiliency.

1 106. (New) The electronic assembly of claim 105 wherein the substrate comprises a
2 material selected from the group consisting of a semiconductor die, a printed circuit board,
3 a plastic substrate, a ceramic substrate, and a teflon based substrate.

1 107. (New) The electronic assembly of claim 105 wherein the first material includes a
2 material selected from the group consisting of gold, aluminum and copper.

1 108. (New) The electronic assembly of claim 105 wherein the elongate element has a
2 cross-dimension of between 0.0007 and 0.003 inches.

1 109. (New) The electronic assembly of claim 105 wherein the second material is
2 connected to the respective terminal.

1 110. (New) The electronic assembly of claim 105 wherein the second material includes a
2 material selected from the group consisting of nickel, cobalt and iron.

1 111. (New) The electronic assembly of claim 105 wherein the second material is
2 between 0.00010 and 0.003 inches thick.

1 112. (New) The electronic assembly of claim 105 wherein the elongate element has a
2 cross-dimension of between 0.0005 and 0.005 inches and the second material is between
3 0.00005 and 0.007 inches thick.

REMARKS

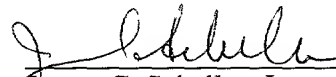
Applicants respectfully request consideration of the application as preliminarily amended herein. No new matter has been added.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 2/5, 1999


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ABSTRACT

An electronic assembly. The electronic assembly includes a semiconductor die which has a plurality of terminals and includes a plurality of interconnection elements. Each interconnection element has a portion attached to a respective one of the terminals and has a resilient, elongate, free standing section which extends from the portion.

03401.P001D3

**METHOD FOR MANUFACTURING RAISED ELECTRICAL
CONTACT PATTERN OF CONTROLLED GEOMETRY**

BACKGROUND OF THE INVENTION

This invention relates in general to electronic assemblies and testing thereof. In particular, the invention relates to a method of manufacture of protruding, controlled aspect ratio and shape contacts for uses in interconnections of assemblies and testing thereof.

Interconnections which involve protruding electrical contacts are used extensively in packaging of electronics. Pin grid array packages, both plastic and ceramic, housing a variety of semiconductors, use area arrays of pins as interconnect contacts for connection to circuit boards. Pins can be attached to their receiving package conductors by use of a variety of methods. For ceramic packages, pins are inserted into non-reacting brazing fixtures and are then gang-brazed to corresponding conductive terminals on the package. This approach is characterized by significant non-recurring engineering costs and lead times involved in production of the brazing fixture. Plastic pin grid array packages most commonly use pins which are inserted into metallized through holes in a circuit board, while the dimensions of pins and the holes normally chosen to facilitate good contact between the

walls of the pins and the coating of the holes. This approach has a disadvantage in that the coated holes and the pins block some circuit routing channels within the circuit board, thus forcing either use of narrow circuit traces, or increase in circuit board area, either of which results in increased costs.

Permanent connection of the pin grid array packages to circuit boards often is accomplished by inserting pins through corresponding holes in a circuit board, the pins protruding to a predetermined length beyond the circuit board. A resulting assembly then is passed through a wave soldering machine, and the pin grid array thus is soldered to the circuit board. Alternatively, a pin grid array can be inserted into a low insertion force or zero insertion force socket for a demountable assembly. Such a socket, in its turn, normally is connected permanently to a board.

A current trend in interconnections is toward face-to-face surface mounting of components to boards and semiconductor chips to substrates. This approach is best accomplished with protruding contact structures on top of (or otherwise protruding from) contact carrying conductive terminals or traces. Conductive terminal arrangements on facing components and substrates are increasingly being made of the area array type, as this allows for larger contact-to-contact separation as compared with components characterized by peripheral arrangement of interconnection contacts.

Pins attached to either ceramic or plastic packages according to the traditional methods are, in general, not appropriate for mounting to patterns of surface contacts on circuit boards, due to pin length variation. For surface mounting, the pins would have to be planarized, which represents an additional expensive step subsequent to pin assembly. In addition, there is a significant cost penalty associated with production of pin-carrying packages with pin-to-pin separations of 50 mils, or lower.

There is currently an increasing need for a low cost method of attaching protruding contacts from conductive terminals, arising from proliferation of surface mountable area array contact packages. Stand-off height of protruding contacts is particularly important when coefficients of thermal expansion of components and of circuit board materials differ significantly. The same is true for attachment of un-packaged semiconductor chips to interconnection substrates. These expansive concerns call for a low cost, high volume method of manufacturing protruding, controlled aspect ratio or shape electrical contacts on top of (or otherwise protruding from) contact carrying conductive terminals, on top of any device or circuit bearing substrate, board material or component, and its applications to surface mount interconnections of devices, components and substrates.

THE PRIOR ART

U.S. Patents 5,189,507, 5,095,187 and 4,955,523 disclose a method of manufacturing of controlled height protruding contacts in a shape of wires for direct soldering to a mating substrate. The wires are bonded to terminals without use of any material other than that of wire and the terminals, using ultrasonic wirebonding methods and equipment, which comprises a standard industry technique for interconnecting semiconductor chips to packages. The patents also describe a bonding head which incorporates a wire weakening means for controlling length of free standing severed wires. Vertically free standing wires present a handling problem during assembly, which is addressed in the patents by providing for polymer encapsulation of bonds between the wires and terminals. The polymer coating, which is optional, also compensates for another disadvantage of the approach, namely weak points along the wire, typically the point of contact between the wire and terminal metallization, and in case of ball bonding, in a heat effected zone of the wire just above impression of a bonding capillary. While these patents provide for controlled height contacts, and discuss 2d to 20 d aspect ratios, in practice they do not assume controlled aspect ratios for all kinds of protruding contacts which are required in various applications. For instance, standard, high speed wirebonding equipment could not handle a 30 mil diameter wire. Therefore, according to these inventions, a 30 mil diameter, 100 mil high contact could only be produced on lower throughput

specialized equipment, at higher cost. In addition, a gold wire as described in a preferred embodiment, would have a problem of dissolving in solder during a soldering cycle, which causes long term reliability problems with solder joints. Similarly, direct soldering of copper contacts would in many cases result in undesirable reaction between copper and solder at elevated temperatures. While nickel metal is the material of choice for solder joint reliability, nickel wire can not be used for ultrasonic wirebonding to metal terminals due to its high mechanical strength and passivating, oxide forming properties. Chemical, physical and mechanical properties, as well as permissible dimensions and shapes of the protuberant contacts produced according to this invention are limited to the capabilities and materials choices compatible with known wire bonding techniques.

U.S. Patent 3,373,481 describes a method of interconnecting semiconductor components on substrates by means of dissolving protruding gold projections on the components in solder masses formed on the substrate terminals. The gold projections are formed by compression and extrusion of gold balls against the terminals. This approach is incapable of producing high aspect ratio protruding contacts because of limitations of the extrusion method. In addition, dissolution of gold in solder, as taught by this approach creates a problem due to reliability concerns. The

method also limits selection of contact material to easily extrudable metals, like gold.

There are several methods in the prior art for controlled elongation of solder masses between a component and a substrate.

5 The goal is to create a column-like solder shape, preferably an hourglass shape, in order to achieve increased resistance to thermal cycling. To that end, U.S. Patent 5,148,968 discloses a web-like device which upon heating during solder reflow stretches the solder connections, forming such hourglass shaped solder
10 columns. Aspect ratios of the columns are determined by the mass of solder in the joint, dimensions of the solder wettable terminals on the substrate and the component, and by the characteristics of the stretch device. This method is only limited to contact materials which are reflowed during the assembly, and requires
15 external hardware for forming the contact shape, which adds cost of the hardware and increases the process complexity.

U.S. Patent 4,878,611 teaches formation of controlled geometry solder joints by applying controlled volumes of solder to a semiconductor package and a substrate, bringing solder masses in
20 contact, and reflowing the solder masses while maintaining controlled separation between the component and the substrate through mechanical means. This approach also requires hardware means for maintaining longitudinal dimension of the contacts, and does not lend itself to standard practices of surface mount

soldering, in which industry already has made substantial time and capital investments.

In the same spirit, U.S. Patent 4,545,610 discloses a process for forming elongated solder joints between a plurality of solder wettable terminals on a semiconductor component and a substrate by means of solder extenders, which transform the shape of the solder joints into uniform hourglass shapes during a solder reflow step. This approach requires additional, non-standard processing of either a silicon device, or on a substrate, which includes attachment of reflowable solder extenders, and non-reflowable means of maintaining vertical spacing between the silicon device and the substrate.

U.S. Patent 5,154,341 discloses the use of a spacer bump which is composed of a solder alloy which does not melt at the temperature of component soldering. A eutectic solder microball is placed on a contact, and upon the reflow the reflowable solder encases the non-reflowable spacer, and produces an hourglass shaped joint. The aspect ratios of the joint contacts are controlled by dimensions of the spacer.

U.S. Patent 4,332,341 teaches solid state bonding of solder preforms to components, substrates or both, for further joining. Resulting protruding solder contacts either collapse during soldering if they consist of eutectic solder, or do not reflow at all, when they consist of higher melting temperature

solder. In the latter case, a component would not have the benefit of a self-alignment effect as the pool of solder is confined to the vicinity of the terminals, and a main portion of the joint which controls aspect ratio, does not melt.

5 U.S. Patent 4,914,814 discloses a process for fabricating solder columns through use of a mold with an array of pin holes, which are filled with solder, brought into contact with terminals on a component, and bonded by melting the solder which wets the terminals. The component with columns is then bonded to a
10 substrate through reflow in a solder with lower melting temperature than the solder of the columns. This approach requires generating a mold for each solder column array pattern, commonly involving undesirable non-recurring engineering expenses and associated increase in delivery times.

15 U.S. Patent 3,509,270 discloses a printed circuit assembly which is interconnected with a dielectric carrier that has a plurality of spring elements positioned in its apertures, the springs are optionally encased in a solder material to facilitate permanent electrical contact between circuit elements. This
20 approach requires a custom interposer pattern manufactured for each application, while solder coated springs would have to be placed individually inside the apertures. Additionally, soldering usually requires flux, while the interposer material makes it difficult to clean flux after the reflow process.

U.S. Patents 4,664,309 and 4,705,205 disclose a device and a method for interconnection with reinforced solder preforms which use a retaining member provided with apertures. The retaining member is optionally dissolved to leave resilient
5 interconnect structures. The solder columns maintain their shape in the molten state, supported by particles, filaments, spiral metallic wire or tape. 20 to 80% by weight of filler material is specified. This approach, as several of the above described approaches, requires a custom made retaining member for every
10 interconnect application, and therefore requires an additional non-recurring engineering expense and increased delivery time for every production order.

U.S. Patent 4,642,889 teaches use of an interposer with a plurality of interconnect areas, interconnect areas comprise wire
15 means surrounded with solder, and incorporate a soldering flux material. Upon heating the solder reflows and connects to the terminals of the mating components and boards, while the wires in the middle of each solder joint insure a column-like joint shape. The interposer preferably is dissolved away. While providing means
20 for controlled aspect ratio interconnect joints, this approach also requires use of a custom manufactured interposer and inevitable non-recurring engineering expenses and increased delivery times. In addition, when improved alignment of the interconnect to the
25 terminals is required, as terminal-to-terminal distances decrease, this approach suffers from heat and environmental effects on

interposer material, causing it to distort or change dimension during processing steps, which makes the alignment difficult, and limits this approach to relatively coarse terminal-to-terminal pitch applications.

5 An elongated protruding solder contact is taught according to U.S. Patent 5,130,779, by sequentially encapsulating solder deposits with a barrier metal. This approach insures that a staged solder deposit does not collapse upon reflow, and a controlled aspect ratio solder contact is thus formed. In order
10 for the barrier metal to be effective, the walls of sequentially deposited solder masses must have a slope. Deposition of such a structure of solder is a lengthy and expensive process.

 A method of attaching highly compliant leads to an array matrix of contacts on a semiconductor package is disclosed in U.S.
15 Patent 4,751,199. An array of leads is manufactured according to the required pattern with temporary tab structures tying the leads together. Compliant leads are gang bonded to terminals on the semiconductor package, and tabs are removed leaving compliant protruding contact leads, ready for attachment to a substrate. This
20 approach, like the interposer techniques described above, requires specific tooling for every new package with a distinct pattern of the array of contacts.

OBJECTS AND SUMMARY OF THE INVENTION

One object of the present invention is to provide a high volume, low cost manufacturing process for production of precise shape and geometry protuberant conductive contacts on a wide variety of electronic components, the contacts having a controlled set of physical, metallurgical and mechanical properties, bulk and surface. Such protuberant contacts can be employed to satisfy electrical, thermal or geometrical requirements in various aspects of electronic interconnection applications. An inventive, multiple stage process is provided, according to which, first, wire stems are bonded to contact carrying terminals, the stems being shaped in three dimensional space, to define skeletons of the resulting contacts. The conductive contacts are then completed through at least one deposition step of a coating which envelopes or jackets the wire skeletons and the terminals. The common coating not only helps to "anchor" protruding contacts to the terminals, but also provides for characteristics of the protuberant contacts with respect to long term stability of their engagement contact with mating electronic component, including but not limited to interconnection substrates, semiconductor devices, interconnect or test sockets. The coating also determines soldering assembly characteristics, as well as long term effects from contact with solder. The coating, along with the wire material properties, determines the mechanical characteristics of the resulting protuberant contact. The wire serves as a "skeleton" of a

resulting protruding contact, and the coating serves as its "muscle".

The wire skeleton can be bonded employing high productivity, highly automated ultrasonic, thermosonic and thermocompression wirebonding equipment (hereinafter referred to as ultrasonic wirebonding equipment). The wirebonding equipment can be organized for ball bonding type ultrasonic wirebonding process, typically used for gold or copper wirebonding, but modifiable for use with other types of single phase or noble metal coated wires. Wire skeletons consisting of free standing single or multiple wire stems can be produced. As required by the shape and dimensions of the resulting conductive contacts, the stems can be standing normal to a terminal or at an angle thereto, or can be formed in three dimensional space. Alternatively, wire skeletons produced using an ultrasonic ball bonder can be produced by forming a traditional wire-bonding loop, which originates and terminates on a contact carrying terminal, or which originates on the contact carrying terminal and terminates on a sacrificial conductor outside the terminal area. Then the sacrificial conductor is selectively dissolved away after completion of the contact processing. Optionally, a wedge bonding type ultrasonic wirebonder can be employed, in which case controlled shape loops of wire are formed with both ends of a loop on the same terminal, or they are formed by originating the loops within the area of the terminal and terminating or severing the loops outside the terminals. Both

types of wirebonders are available commercially from a variety of suppliers, and are highly automated, with the bonding speeds exceeding 10 wires per second.

An additional object of the present invention is to
5 produce array patterns of contacts for unique component designs, without incurring costs and delays associated with production of unique tooling. This object is achieved through use of a sequential wirebonding process for attaching of the wire skeletons to the terminals, and then overcoating them in a single, non-
10 pattern-specific step. Location targeting and geometric characteristics are entered into an electronic control system of the wirebonding equipment with a specific set of commands. A program controls all aspects of wire forming and shaping process, as well as bond characteristics. Production of the multiple
15 protruding contacts does not require time-limiting and costly production of molds, masks or the like, which otherwise would be specific to every incoming order. This object is exemplified by application of the present invention to production of integrated pin-shaped contacts for ceramic pin grid array packages. This is
20 achieved by bonding straight vertical, single stem skeletons, perpendicular to the surface of terminals, and overcoating them with a conductive structural metal or alloy, consisting substantially of nickel, copper, cobalt, iron or alloys thereof, with the thickness of the deposit determined by the required pin

diameter value. This can also be accomplished with equal ease on plastic pin carrying substrates.

Yet another object of the present invention is to create protruding, tower-like solder contacts which substantially maintain a well controlled aspect ratio even when the solder is molten. This object is achieved through first bonding a wire skeleton which will define the final shape and aspect ratio of a solder contact, then optionally overcoating it with a barrier layer which prevents long term reaction between the solder and the wire, and finally depositing a mass of solder which wets the skeleton with a barrier, and clings onto the skeleton even after multiple reflows.

Yet another object of the present invention is to create controlled shape resilient contacts on top of (or otherwise protruding from) contact carrying terminals arranged in various patterns, including arrays. This object is achieved by overcoating the wire skeleton with at least one coating having a predetermined combination of thickness, yield strength and elastic modulus to insure predetermined force-to-deflection characteristics of resulting spring contacts.

Yet another object of the present invention is to produce protruding contacts on top of (or otherwise protruding from) a multitude of contact-carrying terminals, where the terminals have varying vertical coordinates, corresponding to an origin point of

the protruding contacts, while uppermost points of the protruding contacts extend to a vertical coordinate which is substantially identical, within a permissible tolerance level for the component or substrate which is to be subsequently connected to the contacts.

5 In a different embodiment related to this object, the terminals can originate on various electronic components, while the protuberant contacts extend to a substantially identical vertical coordinate. This object is achieved by controlling a wire severing step, by always severing the wire at a predetermined vertical coordinate.

10 Overcoatings then follow the skeleton wire shape and geometry, yielding a contact having a controlled vertical coordinate level, regardless of the Z-axis point of origination of the contact, e.g. plane of the terminal. Some of the terminals having varying Z-coordinates and overlying the component, can optionally and
15 additionally overlie single or multiple electronic devices, placed on the electronic component. In another embodiment related to this object of the present invention, the vertical coordinate of the tips of the protuberant contacts can be controlled by the software algorithm of the appropriately arranged automated wirebonding
20 equipment.

BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing and other objects and advantages will appear more fully from a Summary of the Invention which follows

viewed in conjunction with drawings which accompany and form part of this Application. In the drawings:

Figs. 1a and 1b are schematic representations of a known ball-and-wedge technique to form a loop-like wire skeleton of a protruding electrical contact.

Fig. 2 is a cross-sectional representation of a conductive contact resulting after overcoating the wire skeleton depicted in Fig. 2.

Fig. 3 is a schematic representation of a conductive contacts with skeletons consisting of two loop-like stems.

Fig. 4 is a cross-sectional representation of protruding solder contacts based on loop-like skeletons with a barrier metal between the wire material and the solder.

Fig. 5 is a schematic representation of protruding solder contacts where each skeleton consists of two loop-like stems, the skeleton material overcoated with a barrier layer prior to deposition of solder.

Figs. 6a, 6b and 6c depict a sequence of bonding one end of a loop-like stem to a sacrificial pad, overcoating the skeleton,

and a following step of removal of a sacrificial pad, resulting in protuberant contact with a floating end.

Figs. 7a, 7b and 7c represent schematically a sequence of bonding single stem, perpendicular skeletons to contact-carrying terminals.

Fig. 8 is a cross-sectional view of vertical protuberant contacts produced by overcoating the skeletons depicted in Fig. 7c.

Figs. 9 and 10 schematically represent an example of the skeletons based on two single stems perpendicular to the plane of contact carrying terminals, and the protuberant contacts resulting after overcoating skeletons shown in Fig. 9 with contact material.

Fig. 11 is a schematic representation of protuberant contacts originating on terminals lying in different planes, and with tips of the contacts lying in a common horizontal plane.

Fig. 12 is a schematic representation of protuberant contacts originating on terminals lying two distinct electronic components, and with tips of the contacts lying in a common horizontal plane.

Fig. 13 is a schematic representation of a solder protuberant contact based on a double-stem skeleton, with a barrier layer between the skeleton and the solder.

Fig. 14 is a schematic of S-shaped wire skeletons.

5 Fig. 15 is a cross-sectional representation of resilient protuberant contacts resulting after overcoating the S-shaped skeletons with an appropriate conductive material.

10 Fig. 16 is a cross-sectional representation of resilient protuberant contacts with microprotrusions for improved contact characteristics.

Fig. 17 is a representation of a fence-like skeleton on top of a contact-carrying terminal.

15 Fig. 18 is a cross-sectional representation of a pin grid array package incorporating pins produced according to the method of the present invention.

Fig. 19 is a schematic representation of an assembly of a semiconductor package incorporating protuberant solder contacts and an interconnection substrate, after completion of the solder reflow step.

Fig. 20 is a schematic representation of an electronic package, incorporating protuberant solder contacts on its two interconnection surfaces.

Fig. 21 is a schematic representation of a dielectric interposer with resilient protuberant contacts on its two surfaces, the interposer including means of electrical interconnection between the two surfaces.

DETAILED DESCRIPTION OF THE INVENTION

The method of the present invention relies on wirebonding equipment to produce controlled aspect ratio and shape wire skeletons, which are subsequently overcoated with a desired material in order to produce a required set of properties for protuberant electrical contacts. FIGS. 1A and 1B depict use of a ball-and-wedge wirebonding machine to form a wire skeleton. More detailed descriptions of this and wedge-wedge wirebonding methods, commonly used in the semiconductor industry for interconnecting silicon devices to packages, can be found in Electronic Packaging and Interconnection Handbook edited by Charles A. Harper, on pp. 6.62-6.64 and 7.28-7.30. FIG. 1A illustrates a capillary 1, with an open clamp 2, containing an end portion of a continuous wire 20 with a ball 21 formed at its feed end below the capillary tip. The ball 21 is brought in contact with a contact carrying terminal 90

on top of (or otherwise contained within) a substrate 10. As a result of application of pressure, temperature or ultrasonic energy, or combinations thereof, a ball is bonded to the terminal. The ball 21 in FIG.1A is in the bonding process changed into ball bond 22 shown in FIG.1B. Subsequent capillary motion sets the shape of a wire stem 60. Loop geometry of the wire stem is controlled by parameter settings in the software control algorithm of the wirebonding equipment. A second end of the wire stem is bonded to the terminal 90 by means of pressure, temperature or ultrasonic energy, and deforming the wire against the terminal 90. A wedge-shaped joint 23 thereby is produced. The capillary then rises to a predetermined height, a clamp 2 closes, and the wire is severed at a thinnest point of the joint 23, leaving a fractured free end 24 below the capillary 1. In preparation for forming a next joint, a next ball 21 is formed at the severed free end 24 of the of the wire underneath the capillary, and the cycle is repeated. The loop-shaped stems can be alternatively produced by wedge-wedge technique, where both end of the stem are bonded by the wedge tool. Wire material for ball-wedge and wedge-wedge type of wire-bonding is most commonly gold, aluminum or copper, with slight modifications by other elements, like beryllium, cadmium, silicon and magnesium to control the properties. other materials , including solder, and specifically lead-tin solder wire, have been employed. Alloys of silver and platinum group elements can also be used for wire material. Gold, aluminum and copper, or alloys based on these metals, are the preferred wire materials. The terminal

material should preferably use at least a top layer (if a multi-layer structure) of gold or aluminum, but numerous other metallizations can be successfully used, requiring different levels of ultrasonic vibration, force and temperature. A modern automated wirebonder is capable of bonding over 10 loop-shaped stems per second.

In the embodiment of the present invention represented in FIG. 1A and 1B, a wire skeleton 30 is formed upon severing the single wire stem 60. As shown in FIG. 2, the skeleton 30 geometrically defines the protuberant contact produced by the method of the present invention.

Referring now to FIG.2, physical, finishing and determining mechanical and chemical properties of the resulting protuberant contact 50 are provided through overcoating of the skeleton 30 and the contact carrying terminal 90 with a continuous coating 40, which consists of at least one electrically conducting layer. The continuous coating 40 anchors the skeleton to the terminal by bridging in the areas of contact between the ball bond 22 and wedge bond 23 and the terminal. The overcoating material may be significantly stronger than the skeleton material. It can be applied by wet electrochemical means, e.g. through electrolytic or electroless aqueous solution plating of metals on the skeleton and the terminal. The wet plating techniques are, for instance, described in Metal Finishing Guidebook annually published by Metals

and Plastics Publications, Inc. One preferred embodiment comprises electroplating of nickel out of nickel and its alloys. This method is capable of depositing controlled thickness coating with a tensile strength in excess of 80,000 pounds per square inch.

5 Additional improvement of mechanical strength of the resulting contact is achieved when a coating with a compressive internal stress is deposited, which effectively increases the stress level required to deform or break a resulting protuberant electrical contact.

10 The coating method also optionally includes so-called physical and chemical vapor methods of conductor material deposition. These optional techniques are detailed in a book by M. Ohring, The Materials Science of Thin Films, Academic Press, 1992. Coating methods also include and contemplate deposition of
15 conductors through various decomposition processes of gaseous, liquid or solid precursors.

Nickel has a strong tendency to form an oxide and is , therefore, not the best choice as a contact metal. It requires large contact forces to break through the oxide. For low contact
20 force applications, it requires a second noble or semi-noble coating layer on top. Gold, silver, elements of the platinum group and their alloys are preferred choices as a noble or semi-noble overcoating layer. Similarly, in other instances, multiple layers, comprising conductive overcoating 40, can be selected to tailor the

set of properties of the protuberant contact to a given application.

Within the method of the present invention, a plurality of wire stems can be employed to create a wire skeleton. Referring now to FIG. 3, two wire stems 60 comprise each skeleton 31. Conductive overcoating 40 completes protuberant contact 51.

In a growing number of applications there is a requirement for controlled aspect ratio columns of solder on top of an area array of terminals on ceramic and plastic semiconductor packages. Most often, an area array of balls made of a solder alloy, commonly a eutectic alloy of lead and tin, is used in surface mounting of components with an array of terminals to a matching array of contacts on a circuit board. Long term resistance of such solder contacts is determined by the height, shape and the material characteristics of the solder joints. The method of the present invention provides for controlled shape solder contacts formed around a wire skeleton. Referring now to FIG. 4, wire skeletons 30 are first coated with an optional barrier layer 41, and in the following step are overcoated by a solder mass 42. The barrier layer 41 inhibits an undesired reaction between the wire material and the solder mass. This barrier layer is especially important in the embodiment of the invention providing for gold wire skeletons and a solder mass comprising an alloy of lead and tin. Due to reactivity between gold and tin, and

detrimental effects of intermetallic, gold-tin compounds on the service performance of solder joints, the reaction between gold and tin must be prevented. A 100 to 1,000 microinch barrier of a nickel alloy is typically a sufficient deterrent to the reaction
5 between solder and gold for most applications.

Deposition of solder overcoating can be accomplished, for example, by sending a package or a substrate through a solder wave process cycle in a solder wave equipment. The solder wets the barrier layer 41, and bridges among adjacent wire portions forming
10 the loop-shaped stems, to assume a shape depicted in FIG. 4. The shape does not substantially change during subsequent reflow cycles, as long as there is no significant reaction between the solder and the barrier coating. The overall shape of the solder column depends on area of the terminal and geometry of the
15 skeleton. Bridging by molten solder masses between the ascending and descending branches of loop-shaped stems, and the surface of the terminal, enables the skeletons to contain and support disproportionately large volumes of solder. A preferred solder protuberant contact produced by the method of the present invention
20 will contain more than 70 volume percent of solder, and more preferably over 80 volume percent of solder by volume, with the remainder comprising the skeleton and the barrier material.

A preferred embodiment for a solder column contact, depicted in FIG. 4, is a gold based alloy wire loop skeleton 30,

with a nickel based coating 41, and a near eutectic lead-tin solder mass 42. The gold wire diameter usually ranges between 0.0005 and 0.005 inches, and preferably between 0.0007 and 0.003 inches. The nickel overcoat 40 usually ranges in thickness between 0.00005 and 0.007 inches, and preferably between 0.000100 and 0.003 inches. Another preferred embodiment is a copper alloy wire skeleton, which can be directly overcoated with solder mass. In another preferred embodiment, copper based wire skeleton is first overcoated with a nickel based barrier layer, followed by deposition of the solder material.

Both types of protuberant contacts, solder contacts 52 shown in FIG. 4, and contacts 50 shown in FIG. 2, can be soldered to an interconnection substrate or a component using surface mount soldering technology. Solder contacts 52 have an advantage in handling prior to processing, in that solid solder mass 42, FIG. 4, bridging between the wire branches of the skeleton and surface of the terminal, makes the contact more resistant to mechanical damage in handling, e.g. bending and breakage.

Further increase of the overall proportion of solder material in protuberant solder contacts can be achieved by using multiple wire stem skeletons. Referring now to FIG. 5, two severed stems 60 comprise each skeleton 31 bonded to a contact carrying terminal 90. An optional barrier material 41 is first deposited on top of the terminal 90 and the skeleton 31, in order to inhibit a

possible reaction between the solder and the skeleton material. A solder material 42 completes the construction of a protuberant solder contact. By presenting more ascending and descending wire branches for solder bridging, the relative volume content of solder material in the protuberant contact can be significantly increased.

Protuberant solder contacts 52 and 53 in FIG. 4 and FIG. 5, substantially retain their shape even during subsequent solder reflows, due to strong wetting between the solder and the barrier layer 41. This shape retention is despite solder constituting preferably more than 70 volume percent of the protuberant contacts 52 and 53, and most preferably more than 80 volume percent. This property of protuberant solder contacts produced by the method of the present invention allows for contacts to be put on electronic components prior to device assembly steps, even if the assembly process involves heating the component 10 above the melting temperature of solder material 42.

In the spirit of the present invention, but in yet an alternate embodiment, either the ball or a wedge bond of the wire stem can be formed outside the area of a contact carrying terminal. As shown in FIG. 6A, a wedge bond 29 completes a wire stem 61, and upon the severing step at the bond 29, resulting in formation of a wire skeleton 32. The wedge bond 29 is formed outside the area of the terminal 90 at a terminal 91. The terminal 91 is positioned on top of a sacrificial layer 80, or could optionally be sacrificial

by being comprised of a dissimilar metal from that of terminal 90. In both cases, the sacrificial arrangement is temporarily used for electrical contact to an electrode, in that it is required for electroplating of a conductive coating 43, enveloping the skeletons 32 and the terminals 90 and 91, to complete protuberant conductive contacts 54, as illustrated in FIG 6B. The sacrificial layer is also used for assuring a different Z-axis coordinate for the bonds 22 and 29. As illustrated in FIG. 6C after a coating deposition and a removal step for the sacrificial layer, one of the ends of the wire loop based contact ends up being suspended above the substrate 10. This suspension is especially important for formation of controlled geometry spring contacts, capable of resiliently engaging with mating terminals on a component or a substrate for testing, burn-in or demountable electrical interconnect in service. The variation of the Z-coordinate of an end of the wire-loop shaped contact allows for resilient Z-axis movement of a tip of the resulting spring when a force is applied thereto.

Referring now to FIGs. 7A through 8, another preferred embodiment of the method for manufacturing controlled aspect ratio protuberant contacts. In this embodiment, only ball bonding is used, the wires are bonded substantially vertically. The software of the control system of the wirebonder is programmed to exclude the common wedge bonding step for severing the wires. Instead the same electronic or hydrogen flame-off used for ball formation prior to the ball bonding is employed to severe the wires at a

predetermined height. FIG. 7A depicts a ball bonding capillary 1, wire 20 after the first ball bonding step which created a ball bond 22 on top of contact carrying terminal 11. After the bond the capillary moves up to a predetermined position, and electrode 3 is brought under a high potential, resulting in a generation of a spark which melts and severe the wire 20 at a predetermined spot, as illustrated in FIG. 7B. As a result of the severing step a wire skeleton 33 is created, which comprises a single stem 62 extending to its severed end 25. The severing step also readies the feed end 21 of the wire 20 to the next stem bonding step.

After vertical wire stems 62 are formed on contact carrying terminals 90, and after the severing step which defines single stem skeletons 33, as shown in FIG. 7C, the wire skeletons 33 and the contact carrying terminals 90 are overcoated with a deposit 44, originating at the terminals and extending to the tops of the wires as a continuous blanket coating, to complete protuberant contacts 55. As in the previous embodiments, the composition and the thickness of the continuous coating is selected to satisfy requirements of a given application.

The protuberant vertical contacts 55 shown in FIG. 8 are especially useful as a replacement method for standard techniques for attachment of pins to plastic and ceramic semiconductor packages, a method which results in lower package cost and reduced pinned package production time. This usefulness is due to the fact

that the pin-shaped contacts produced by the method of the present invention do not require pattern specific tooling or molds.

Thickness and material composition of conductive layer 44 shown in FIG. 8 depends on the production, assembly and service requirement, and the characteristics of package material. The layer 44 can comprise nickel alloy. In another embodiment layer 44 can comprise a copper alloy. Yet in another embodiment layer 44 can comprise alloys of nickel, iron and cobalt, with controlled thermal expansion characteristics. In yet additional embodiment, layer 44 is formed through multiple deposition steps, the top deposit comprising a noble or semi-noble metal or alloy, out of a group of gold, platinum, silver, rhodium, ruthenium and copper. The top deposit improves electrical contact characteristics of the pin-like contact 55.

FIGs. 9 and 10 illustrate the process of attachment of contacts 56 to the contact carrying terminals 90. The contacts are based on skeletons 34, each skeleton 34 comprising two vertical wire stems 62. The skeletons and the terminals 90 are overcoated by layer 44 to complete protuberant, vertical-double-stem contacts 56. This type of contacts can be useful in soldering, where multiple branches of the contact present themselves to solder for improved assembly yield.

Referring again to FIG. 7b, due to the stationary position of the electrode 3, the wire 20 always gets severed at a predetermined elevation, regardless the Z-coordinate of the ball bond 22. Uniform elevation is a very desirable property, especially in cases when contacts are placed on a substrate or a component which is not planar due to manufacturing tolerances, or is warped during thermal assembly or processing steps. In other instances, various terminals 90 on an electronic component 10 are provided in different planes, as illustrated in Fig. 11, while the highest elevation points of the protuberant contacts 56 may be required to lie in a substantially identical horizontal plane. This configuration is especially important when contact carrying terminals lie at different positions of an interconnection substrate, but they bear protuberant contacts which must contact a highly planar device or component. This self-planarizing capability of the method of the present invention is also important when the terminals lie on different components, but the tips of protuberant contacts must terminate in a substantially identical horizontal plane. As illustrated in FIG. 12, terminals 90 and 92 lie on components 10 and 11 respectively, while the protuberant contacts 56 vertically terminate in a substantially identical plane. In the embodiment depicted in FIG. 12, the two components, 10 and 11, are optionally interconnected electrically by means of conductive masses 900. In this embodiment of the invention, component 11 can represent an interconnection substrate, while one or a plurality of components 11 can represent passive devices,

like capacitors or resistors. The protuberant contacts may serve for interconnection of a bare, unpackaged semiconductor device (not shown in the FIG. 12) to the interconnection substrate 10. Such an electrical arrangement decreases the inductance value between critical contacts on the semiconductor device and a capacitor component, which improves electrical performance of semiconductor devices operating at high frequencies.

Wire skeletons consisting of multiple vertical wire stems are especially useful for protuberant solder contact applications. FIG. 13 illustrates a cross section of a protuberant solder contact 57 supported by a wire skeleton 34 consisting of two vertical stems 62. An optional barrier layer 45 is first deposited, followed by the solder 42 deposition step. The deposition step can be accomplished by passing a substrate with protuberant skeletons 34, overcoated with a barrier metal 45, through a common wave soldering machine. The solder bridges between the wires, in addition to coating the outsides and top surfaces of the wires due to wetting. In contrast, a single wire skeleton would support solder only through the wetting mechanism, without a possibility of bridging, and would support less bulk solder per wire as a result of solder application by solder wave technique. A common solder state-of-the-art wave soldering method is described in Electronic Materials Handbook, Volume 1 Packaging, from ASM International, Materials Park, Ohio, on pp. 688 through 696. A preferred method for producing this embodiment of the present invention involves use

of multiple gold wires, ranging from 0.0005 to 0.005 inches in diameter, and more preferably 0.0007 to 0.003 inches in diameter, overcoated with 0.000030 to 0.005 inches of nickel or nickel alloy or cobalt or cobalt alloy, and more preferably with 0.000050 to 0.003 inches of nickel or nickel alloy. The amount of solder deposited from solder wave would depend on the wave conditions and the dimension of the overcoated skeleton, as well as the size of the contact carrying terminal.

In another embodiment of the present invention, the solder overcoat 42, which completes a protuberant solder contact 57, is a continuous coating deposited over a wire skeleton without a barrier layer between the solder and the wire and terminals. Gold wire without a barrier is not an appropriate choice for this embodiment, because a continuous reaction between the solder and the gold embrittles the solder or solder joint to a substrate or to a component. However, a copper wire is useful to form the wire skeleton, and then a solder coating is applied using, for example, a solder wave approach referred to above.

An alternative approach to forming solder columns on top of wire skeletons is to plate electrolytically the solder. The electrolytically deposited solder is appropriate for standard surface mount assembly. Alternatively, the solder can be reflowed after electrolytic deposition, and prior to the assembly.

The method of forming wire skeletons described by means of ball bonding, shown in FIGS. 7A through 7C is appropriate for forming individual resilient contacts. Instead of forming vertical wires, the skeleton wires for spring contacts are formed with the shape which deviates from vertical. One preferred embodiment for forming spring contacts is illustrated in FIGS. 14 and 15. A single or multiple wire is ball bonded to a terminal, followed by a motion of the capillary which forms the wire into an S-shaped wire stem 63. The forming step is followed by a severing step performed by means of an electronic flame-off provided by the tool 3. The severing step defines a skeleton consisting of single or multiple S-shaped wire stems. The skeletons 35 and the resilient contact carrying terminals 90 are then overcoated by a conductive deposit 46, which possesses mechanical characteristics, which, along with the s-shape of the stems, ensures a resilient response of the resulting protuberant contacts 58 to a deflective force.

One preferred embodiment for a resilient overcoating is a nickel or a nickel alloy layer. For example, Ni electroplated out of standard nickel sulfamate solution could be used. Such nickel deposit can be produced with compressive internal stress which would improve the spring characteristics, as well as varying strength and ductility levels. A plated nickel-cobalt alloy has greater strength and improved resilient properties. Rhodium, ruthenium or other elements of the platinum group and their alloys with gold, silver and copper constitute another group of preferred

embodiment overcoat materials. Tungsten or nickel can also be deposited by chemical vapor deposition techniques, and represent another preferred embodiment materials.

5 Gold is the most commonly used wire material for ultrasonic wirebonding applications, but it is soft and it may not be an appropriate skeleton material for a spring contact if it constitutes a significant portion of the spring cross-sectional area. One embodiment of the present invention provides for a common high speed bonding of gold skeleton wires. An alloying layer is then deposited, which when reacted with gold, forms a gold alloy, the alloy having higher strength than pure gold. One preferred embodiment provides for deposition of tin on top of gold wire, with subsequent reaction of gold and tin at a temperature below the melting temperature of gold-tin eutectic. A gold-tin alloy results, which is significantly stronger than gold.

10 The contact properties of the springs in FIG. 15 can be enhanced by overcoating the spring material with a noble or semi-noble material, like gold, silver, or elements of the platinum group and their alloys. This overcoating reduces contact resistance of spring contacts 58 when they are engaged against the mating conductive terminals for interconnection purposes. Another embodiment is depicted in FIG. 16, with a spring coating 47 having local protrusions. Such a coating can be created through dendritic growth of an electroplated deposit, or through incorporation of

foreign particulates into the conductive deposit 47. Alternatively, a regular uniform first deposit layer can be applied, which provides for resilient properties, and the subsequently deposited top layer incorporates local protrusions or particulates to complete conductive deposit 47. The local protrusions dramatically increase the local pressures exerted by the resilient protruding contact 59 onto a mating terminal during the interconnection engagement, and reduce contact resistance when contacting easily passivating, oxide forming materials overlying the engaged terminals.

Resilient protuberant contact produced by the method of the present invention rely on the shape of a skeleton and the properties of the conductive material for its spring properties. In another embodiment of the present invention, wire stems or wire skeletons can be additionally shaped by a tool external to a wirebonding equipment, prior or before the deposition step.

FIG. 17 illustrates another embodiment of the present invention, where a fence-like skeleton 36 is erected on top of a contact carrying terminal 90. The skeleton is formed by sequential bonding of the additional loop-shaped wire stems, without the severing steps between the bonds, until the skeleton is completed. This skeleton shape is especially useful when large masses of solder have to be contained within the spacial boundaries determined by the skeleton 36. One preferred application of this

embodiment is production of massive solder pads for thermal interconnection to heat sinks or substrates.

Protuberant contacts, as manufactured according to the present invention, are mounted on terminals on top of various interconnection substrates; such as laminate printed circuit boards, Teflon based circuit boards, multi-layer ceramic substrates, silicon based substrates, varieties of hybrid substrates, and other substrates for integration of electronic systems known to those skilled in the art. The contacts can also be put on top of terminals directly on semiconductor devices, such as silicon and gallium arsenide devices, for subsequent demountable or permanent attachment to interconnection substrates. The contacts could also be put on terminals on one or both sides of electronic components or devices, such as ceramic and plastic packages housing semiconductor components, and other devices. The contacts could be put directly on top of passive devices, such as resistors and capacitors.

One arrangement is shown in Fig. 18, wherein pin-like contacts 55 are mounted to terminals of a ceramic semiconductor package 100. A semiconductor device 101 is bonded to the package using die attach material 112. Electrical interconnection of the device to the package is accomplished with industry standard gold wire bonds 113, extending from terminals 95 on the semiconductor device 101 to terminals 94 on the ceramic package 100. This could

alternatively be accomplished with aluminum wire wedge bonding. Interconnectivity within the ceramic package 100, between the wirebond terminals 94 and pin contact carrying terminals 96, is accomplished through the incorporation of conductors 910 within the ceramic package body. The device is sealed hermetically in a package cavity with, typically, a Kovar alloy lid 111. The contacts can be put on before or after attachment and assembly of the semiconductor device 101 in the package 100. The package 100 with the protruding contacts is ready for interconnection to an interconnection substrate, such as a printed circuit board. Depending on the method of connection, top deposit layer of the contacts 55 can be gold, noble or semi-noble material, tin, or a tin-lead solder alloy.

FIG. 19 illustrates a cross section of a semiconductor package 100, with semiconductor device 101 interconnected therewith, and interconnected to a substrate 12 with terminals 97, the pattern of the terminals 97 matching the pattern of contact carrying terminals 96 on the package 100. The package solder column shaped joints between the terminals 96 and 97 resulted after solder attachment of the package 100 with columns 57 to a substrate 12. The surface mount soldering is accomplished preferably by stencil or screen printing solder paste volumes on top of each terminal 97, positioning contacts 57 in contact with said solder paste, reflowing the solder in an oven at a temperature above the melting temperature of the solder in solder paste. Alternatively

solder can be applied by various means of deposition, and placement and reflow methods can be utilized. The soldering process follows industry standard procedure commonly referred to as Surface-Mount Technology, and described in Chapter 9 of Electronic Packaging and
5 Interconnection Handbook, edited by Charles A. Harper, and published by McGraw-Hill, Inc. As shown in FIG. 19, an hour-glass joint shape results, which is commonly recognized as the most reliable shape for increased resistance to thermally induced joint failures.

10 Due to the fact that columns 57 can be manufactured with any solder, including eutectic tin-lead solder, the most of the volume of the contact 57 melts during the reflow, and the solder redistributes itself according to the surface area of its reinforcing wire skeleton and the surface area of its mating
15 terminals. This feature allows one to achieve in some cases a self-alignment effect, e.g. the component is pulled into registration with the terminal array on a substrate due to surface forces of solder wetting. This feature is in contrast to other techniques for solder column utilization, which typically use
20 higher melting temperature solder, and only the portions of solder deposited onto terminals on a substrate melt, which reduces the absolute value of solder-surface wetting forces.

The contacts of the present invention can be put on both sides of an electronic package or a substrate for multiple

interconnection to various devices. Alternatively, the contacts can be put on one side of an electronic package for interconnection to a semiconductor device, and on the other side of the semiconductor package for subsequent interconnection to a circuit board or any other substrate. FIG. 20 depicts an electronic package 110, which has miniature solder columns 570 for interconnecting a flip-chip semiconductor device, on top of the package, and solder columns 57 at the bottom, for subsequent interconnection to a printed circuit board.

Spring contacts produced by the present invention are used as a standard means of interconnect between substrates and components which have matching patterns of terminals. In many cases it is desirable not to manufacture the contacts on either substrates or components, or devices, as the process yield associated with contact manufacturing would cause loss of costly devices, substrates or components. One embodiment of the present invention, illustrated in FIG. 21, provides for a substantially planar interposer 120 with matching set of terminals 97 on both sides thereof, and means 930 for connecting electrically the matching terminals on both of the sides. Protuberant resilient contacts 59 are placed on contact carrying terminals 97 on both sides of the interposer 120. This contact carrying structure is ready for demountable interconnection of a variety of electronic components.

It will be apparent to those skilled in the art that wide deviations may be made from the foregoing preferred embodiments of the invention without departing from a main theme of invention set forth in claims which follow herein.

I CLAIM:

1 - 1. A method for mounting a protuberant conductive contact to an
2 electronic component, the method comprising sequential steps
3 of:

4 providing a wire having a continuous feed end,
5 intimately bonding the feed end to the component,
6 forming from the bonded feed end a stem which protrudes
7 from the component and has a first stem end thereat,
8 severing the stem at a second stem end to define a
9 skeleton,

10 depositing a conductive material to envelope the skeleton
11 and adjacent surface of the component.

1 2. The method as claimed in Claim 1, and immediately before the
2 severing step intimately bonding the second stem end to the
3 component.

1 3. A method for mounting a protuberant conductive contact to an
2 electronic component, the method comprising sequential steps
3 of:

4 providing a wire having a continuous feed end,
5 intimately bonding the feed end to the component,
6 forming from the bonded feed end a stem which protrudes
7 from the component and has a first stem end thereat,

8 severing the stem at a second stem end to define a
9 skeleton,

10 depositing a conductive material to jacket the skeleton
11 and adjacent surface of the component.

1 4. The method as claimed in Claim 1, and immediately after the
2 severing step, continuing sequentially the bonding step and
3 the forming step and the severing step for a predetermined
4 number of stems to comprise the skeleton.

5 5. The method as claimed in Claim 4, and immediately before each
6 of the severing steps each of the second stem ends is inti-
7 mately bonded to the component.

1 6. A method for mounting a protuberant conductive contact to a
2 conductive terminal on an electronic component, the method
3 comprising sequential steps of:

4 providing a wire having a continuous feed end,

5 intimately bonding the feed end to the terminal,

6 forming from the feed end a stem which protrudes from the
7 terminal and has a first stem end thereat,

8 severing the stem at a second stem end to define a
9 skeleton,

10 depositing a conductive material to envelop the skeleton
11 and adjacent surface of the terminal.

- 1 7. The method as claimed in Claim 6, and immediately before the
2 severing step intimately bonding the second stem end to the
3 terminal.
- 1 8. The method as claimed in Claim 6, and immediately after the
2 severing step, continuing sequentially the bonding step and
3 the forming step and the severing step for a predetermined
4 number of stems to comprise the skeleton.
- 1 9. The method as claimed in Claim 8, and immediately before each
2 of the severing steps each of the second stem ends is inti-
3 mately bonded to the terminal.
- 1 10. A method for mounting a protuberant conductive contact to a
2 conductive terminal on an electronic component, the method
3 comprising sequential steps of:
4 providing a wire having a continuous feed end,
5 intimately bonding the feed end to the terminal,
6 forming from the bonded feed end a stem which protrudes
7 from the terminal and has a first stem end thereat,
8 severing the stem at a second stem end to define a
9 skeleton,
10 depositing a conductive material to jacket the skeleton
11 and adjacent surface of the terminal.

- 1 11. The method as claimed in Claim 10, and immediately before the
2 severing step intimately bonding the second stem end to the
3 terminal.
- 1 12. The method as claimed in Claim 10, and immediately after the
2 last mentioned severing step continuing sequentially the
3 bonding step and the forming step and the severing step for a
4 predetermined number of stems to comprise the skeleton.
- 1 13. The method as claimed in Claim 12, and immediately before each
2 of the severing steps each of the second ends is intimately
3 bonded to the terminal.
- 1 14. A method for mounting a protuberant conductive contact to a
2 conductive terminal on an electronic component, the method
3 comprising sequential steps of:
4 providing a wire having a continuous feed end,
5 intimately bonding the feed end to the terminal,
6 forming from the bonded feed end a stem which protrudes
7 from the terminal and has a first stem end thereat,
8 intimately bonding a second stem end to the terminal,
9 sequentially continuing the forming step and the bonding
10 step for a predetermined number of times,
11 after the last bonding step severing the stem to define
12 a skeleton,

13 depositing a conductive material to envelop the skeleton
14 and adjacent surface of the terminal.

1 15. A method for mounting a protuberant conductive contact to a
2 conductive terminal on an electronic component, the method
3 comprising sequential steps of:

4 providing a wire having a continuous feed end,
5 intimately bonding the feed end to the terminal,
6 forming from the bonded feed end a stem which protrudes
7 from the terminal and has a first stem end thereat,

8 bonding a second stem end to a sacrificial member mounted
9 in spaced relationship from the component,

10 severing the stem at the second stem end to define a
11 skeleton,

12 depositing a conductive material to envelop the skeleton
13 and at least adjacent surface of the component,

14 eliminating the sacrificial member.

1 16. The method as claimed in Claim 15, wherein during the elimi-
2 nating step the second stem ends are severed from the sacrifi-
3 cial member.

1 17. The method as claimed in Claim 6, 7, 8, 9, 14 or 15, performed
2 on a plurality of the terminals on the electronic component.

1 18. The method as claimed in Claim 17, performed on a plurality of
2 wires on a plurality of the terminals on the electronic
3 component.

1 19. The method as claimed in Claim 17, with the bonding performed
2 by applying at least one of a group consisting of superambient
3 pressure, superambient temperature and ultrasonic energy.

1 20. The method as claimed in Claim 17, wherein the severing is
2 performed by melting the wire.

1 21. The method as claimed in Claim 17, wherein the forming steps
2 and the severing steps are performed by a wirebonding appara-
3 tus, and after the severing steps but before the depositing
4 step shaping the skeleton by means of a tool external to the
5 apparatus.

1 22. The method as claimed in Claim 17, wherein the severing of the
2 second ends is performed by mechanical shearing.

1 23. The method as claimed in Claim 17, wherein during the forming
2 step the shape of the stems is determined by means of a
3 software algorithm in a control system of an automated
4 wirebonding apparatus.

1 24. The method as claimed in Claim 6, 7, 8, 9 or 15, performed on
2 a plurality of the terminals, wherein shape of the skeleton
3 and mechanical properties of the conductive material are
4 organized collectively to impart resilience to the protuberant
5 conductive contact.

1 25. The method as claimed in Claim 24, wherein the conductive
2 material is provided with a multitude of microprotrusions on
3 its surface.

4 26. The method as claimed in Claim 17, with the depositing step
5 including placement of a plurality of layers each differing
6 from one another.

1 27. The method as claimed in Claim 24, wherein the depositing step
2 includes placement of a plurality of layers each differing
3 from one another.

1 28. The method as claimed in Claim 27, wherein at least one of the
2 layers comprising conductive material has a jagged topography
3 in order to reduce contact resistance of the protuberant
4 conductive contact when mated to a matching terminal.

1 29. The method as claimed in Claim 17 or 24, wherein the
2 deposition is performed by means of electrochemical plating in
3 an ionic solution.

1 30. The method as claimed in Claim 6 or 8, performed on a plurali-
2 ty of the terminals and, wherein:

3 the forming steps result in a plurality of free-standing
4 protuberant stems,

5 the severing steps are performed on the respective stems
6 all in a common plane.

1 31. The method as claimed in Claim 6 or 8, performed on a
2 plurality of the terminals on at least one electronic
3 component and, wherein:

4 the terminals are in different planes,

5 the forming steps result in a plurality of free-standing
6 protuberant stems,

7 the severing steps are performed on the respective stems
8 all in a common plane.

1 32. The method as claimed in Claim 6 or 8, performed on a
2 plurality of the terminals on at least one electronic
3 component, wherein shapes of the skeleton and mechanical
4 properties of the conductive material are organized
5 collectively to impart resilience to the protuberant
6 conductive contacts, and the severing steps are performed on
7 the stems all in a common plane.

1 33. The method as claimed in Claim 17 or 24, wherein the cross-
2 sectional area of the wire is rectangular.

1 34. The method as claimed in Claim 26 or 27, wherein the wire is
2 made of a metal selected from a group consisting of gold,
3 silver, beryllium, copper, aluminum, rhodium, ruthenium,
4 palladium, platinum, cadmium, tin, lead, indium, antimony,
5 phosphorous, boron, nickel, magnesium and alloys thereof, and
6 wherein at least one of the layers of the conductive material
7 is a metal selected from a group consisting of nickel,
8 phosphorous, boron, cobalt, iron, chromium, copper, zinc,
9 tungsten, tin, lead, bismuth, indium, cadmium, antimony, gold,
10 silver, rhodium, palladium, platinum, ruthenium and alloys
11 thereof.

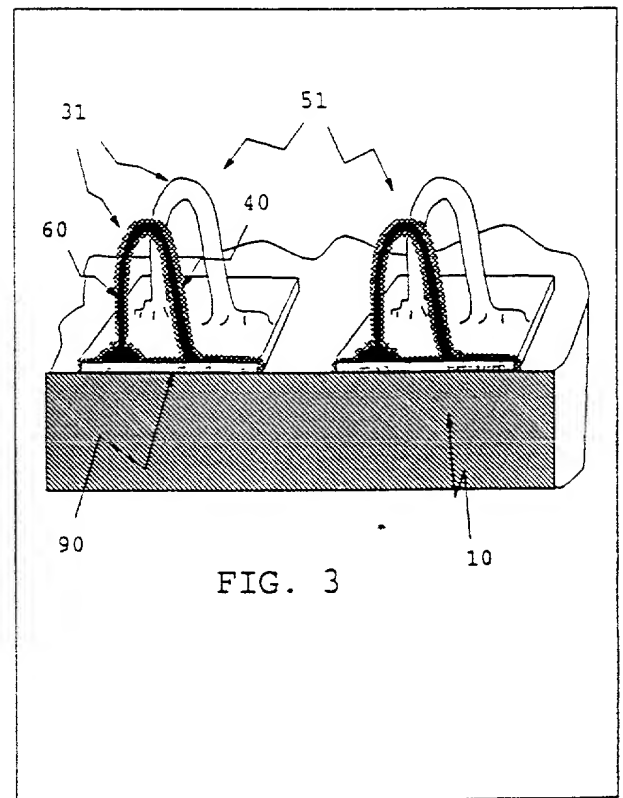
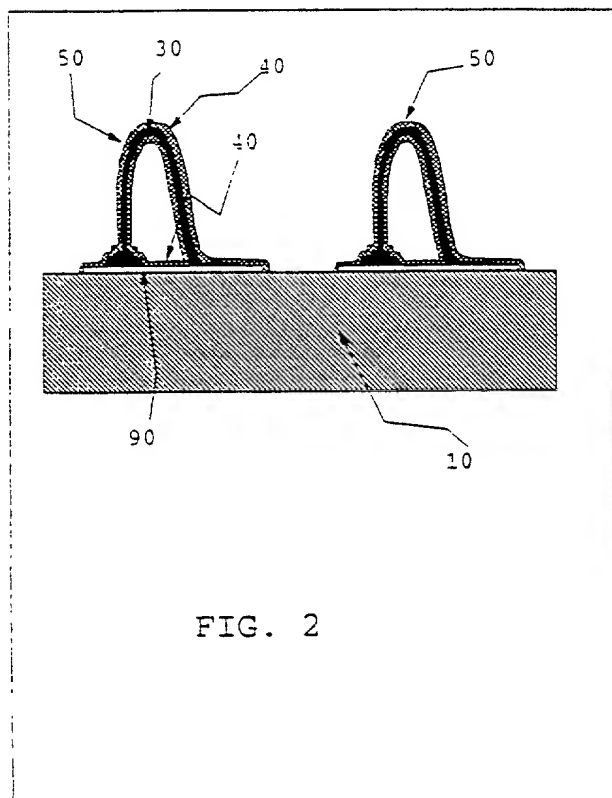
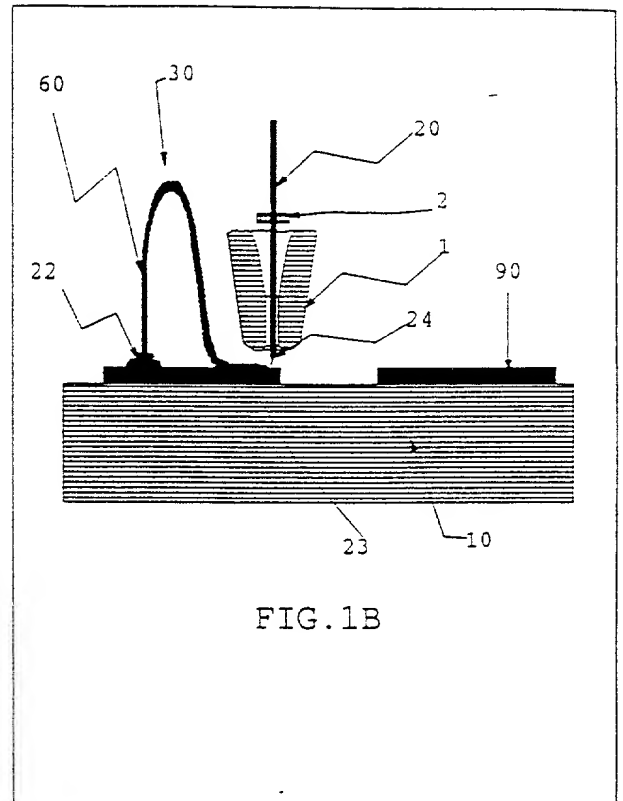
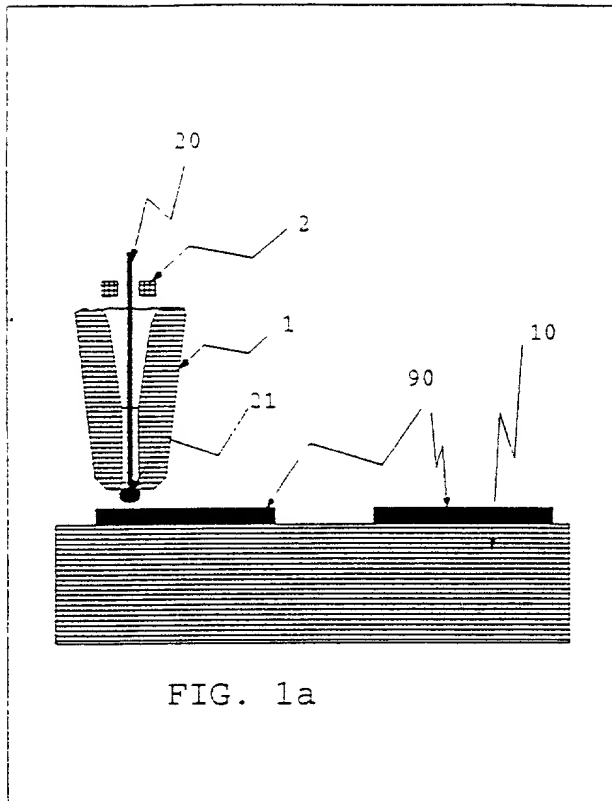
1 35. The method as claimed in Claim 6, 7, 8, or 14, performed on at
2 least one terminal on an electronic component, wherein the
3 wire is made primarily of a metal selected from a group
4 consisting of gold, copper, aluminum, silver, lead, tin,
5 indium and alloys thereof; the skeleton is coated with a first
6 layer of the conductive material selected from a group
7 consisting of nickel, cobalt, boron, phosphorous, copper,
8 tungsten, titanium, chromium, and alloys thereof; a top layer
9 of the conductive material is solder selected from a group
10 consisting of lead, tin, indium, bismuth, antimony, gold,
11 silver, cadmium and alloys thereof.

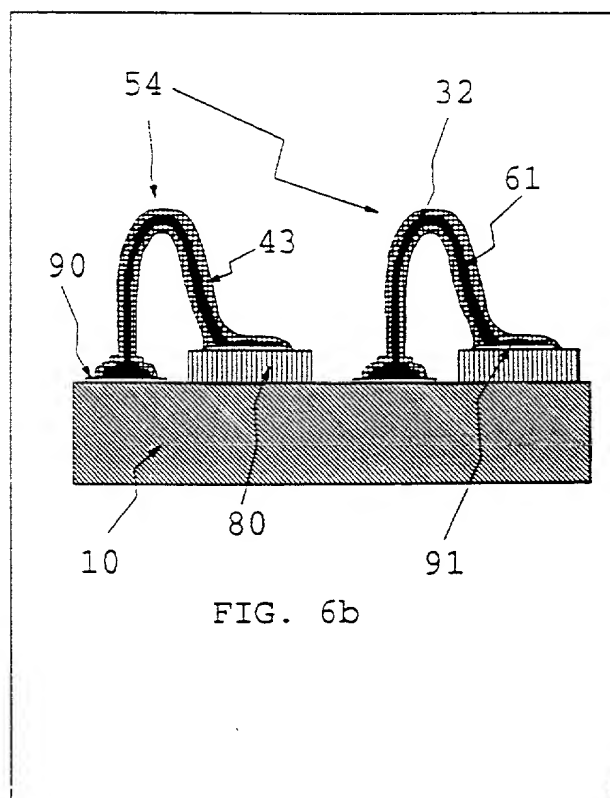
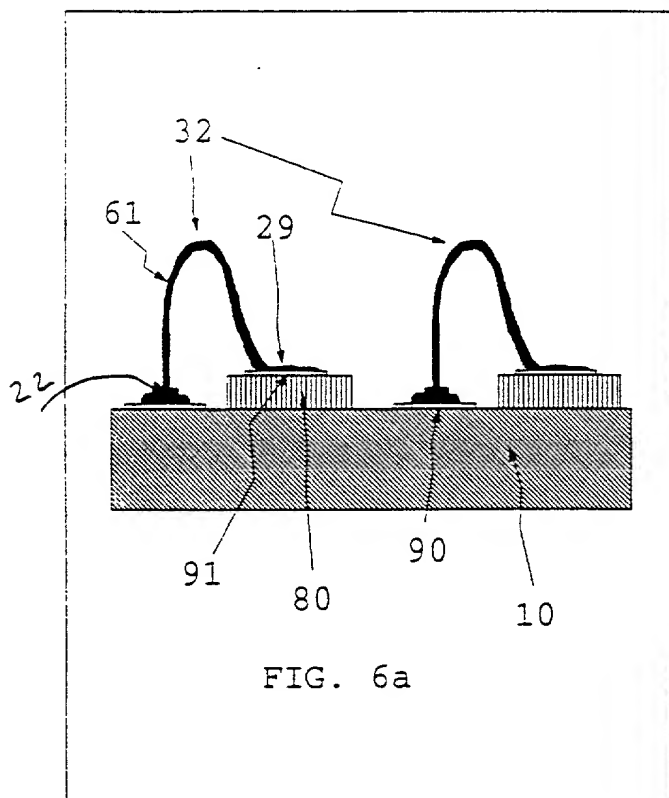
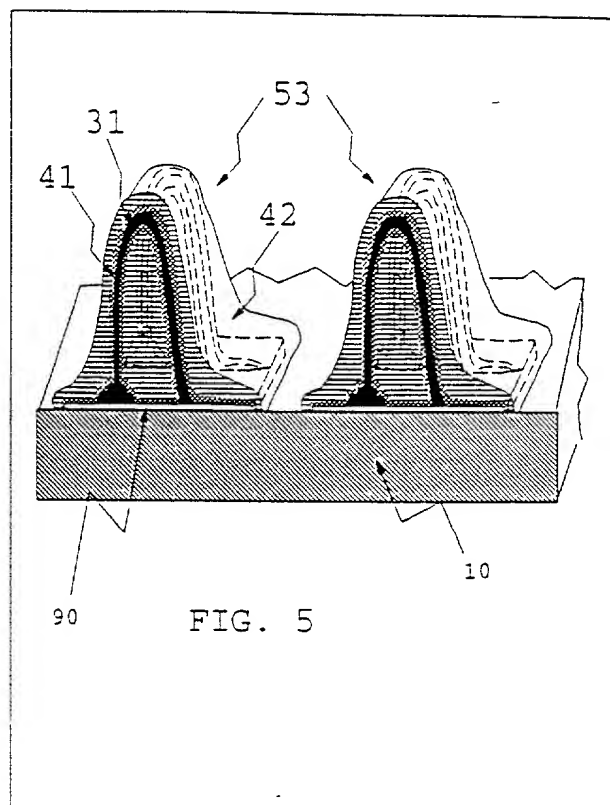
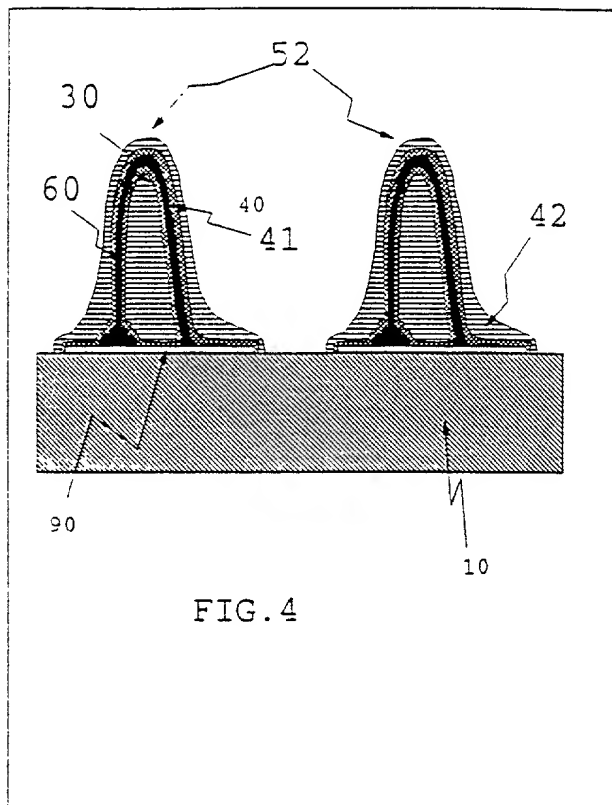
1 36. The method as claimed in Claim 26 or 27, wherein a layer
2 reactive with material of the wire is interposed between the
3 skeleton and the conductive material.

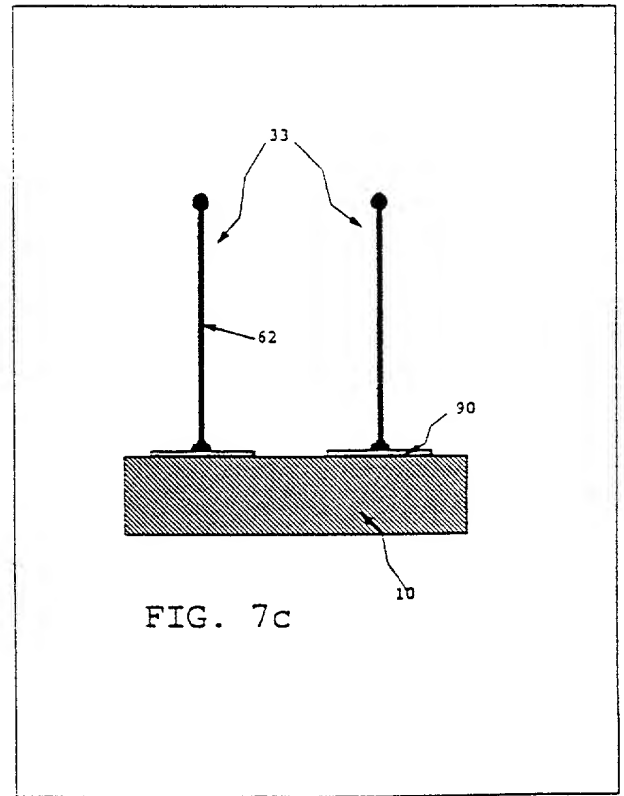
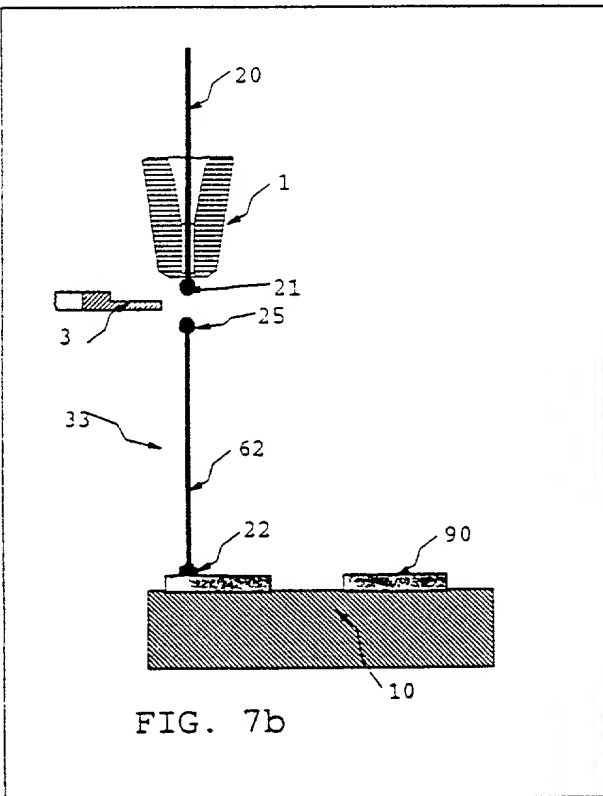
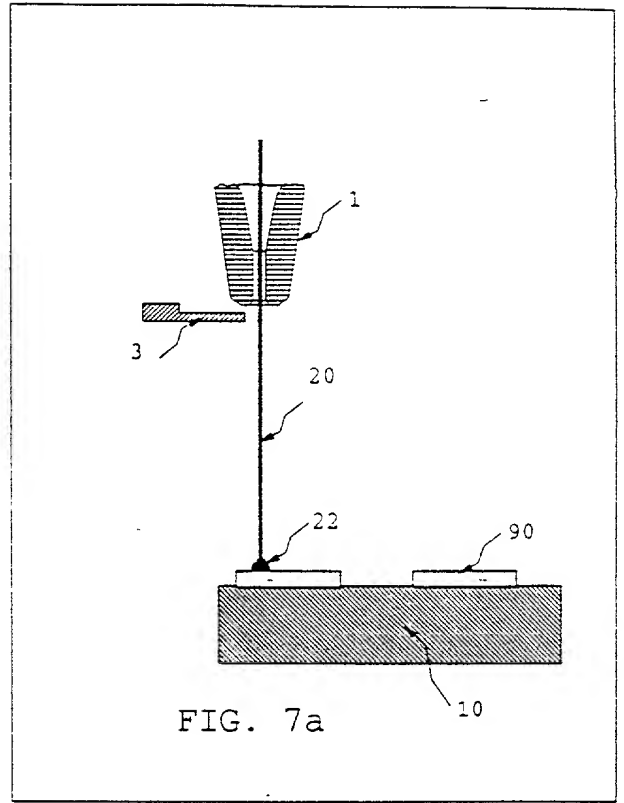
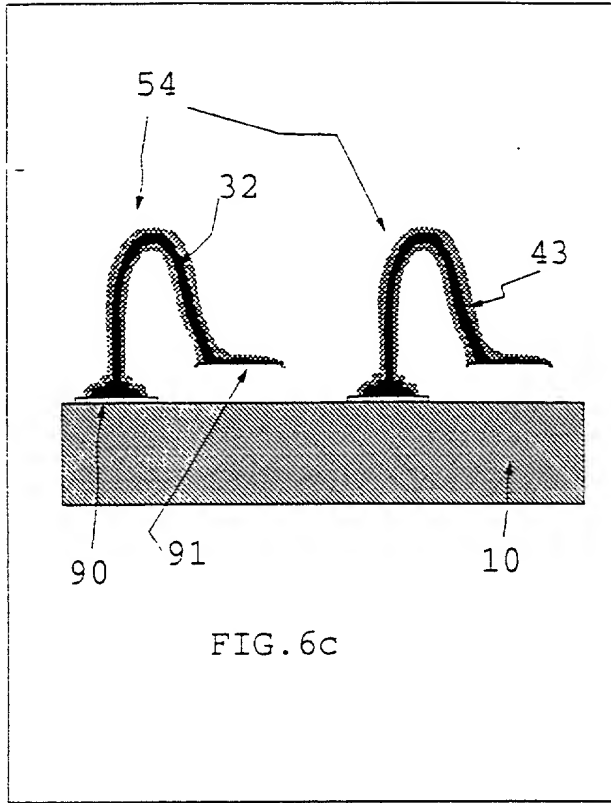
1 37. The method as claimed in Claim 26 or 27, wherein the wire is
2 gold and the reactive layer is tin.

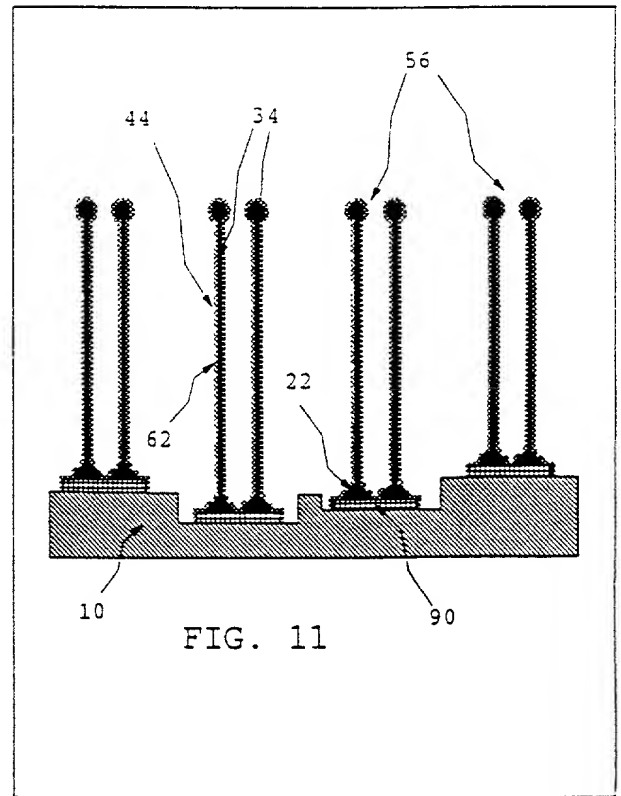
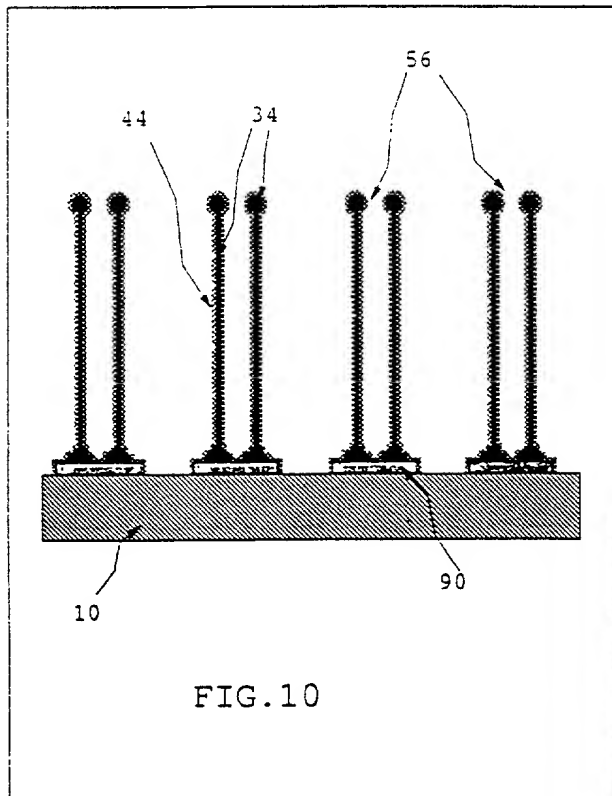
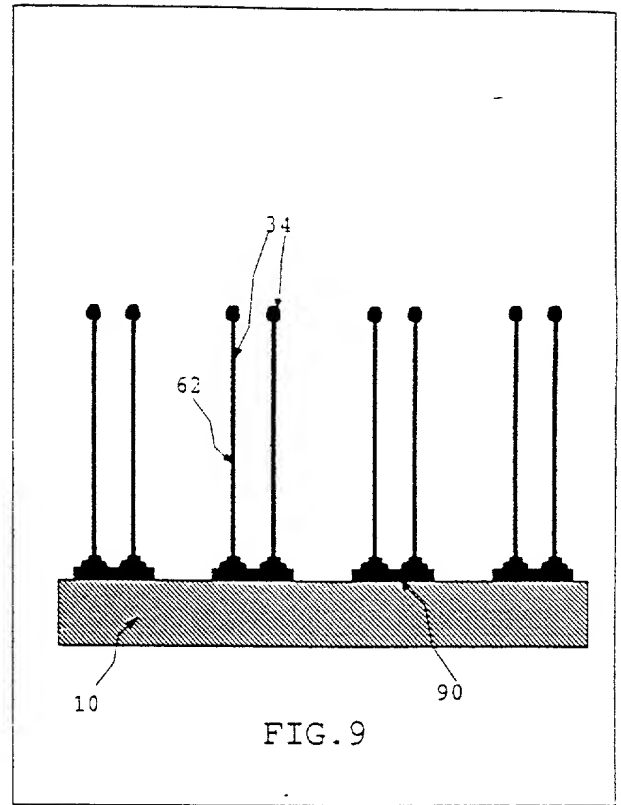
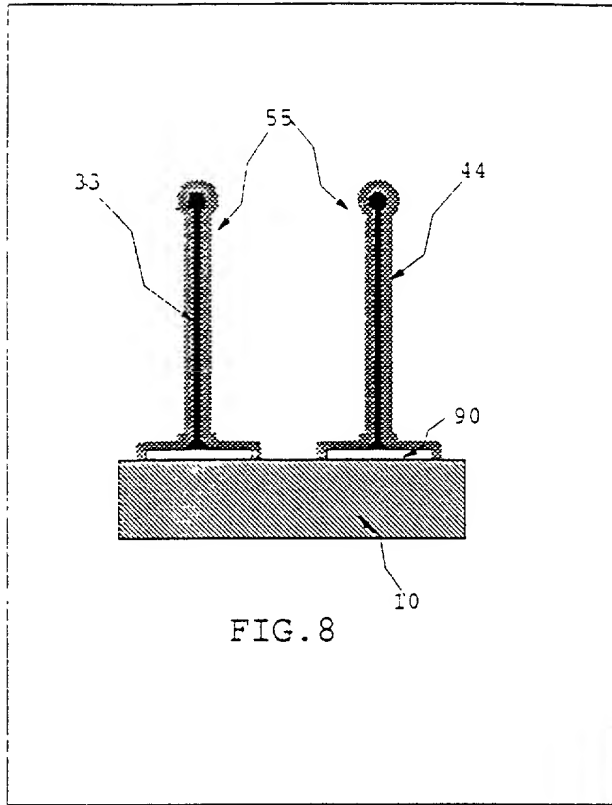
1 38. An electronic component a first and a second surface in which
2 on at least one of the surfaces is provided a plurality of the
3 terminals having protuberant conductive contacts mounted
4 thereto and made by means of the method as claimed in any of
5 Claims 6, 7, 8, 14, 15 or 34.

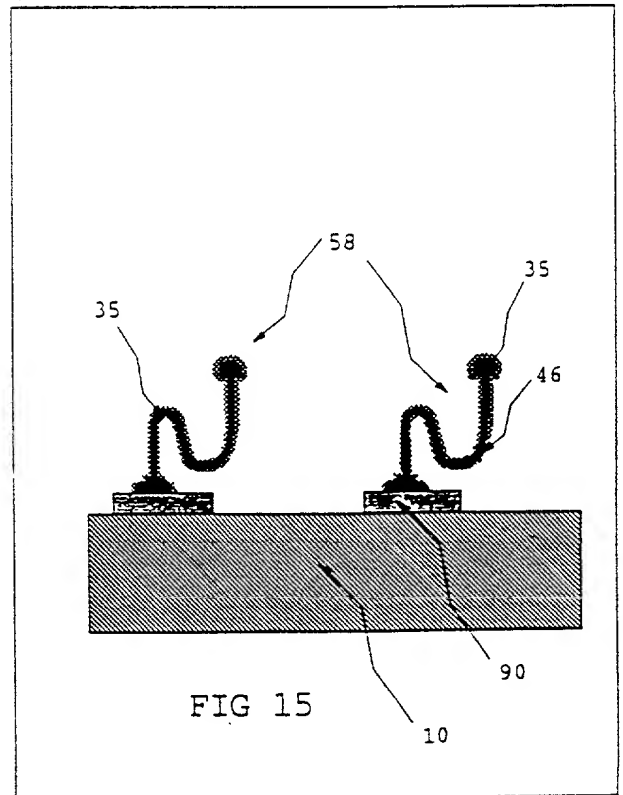
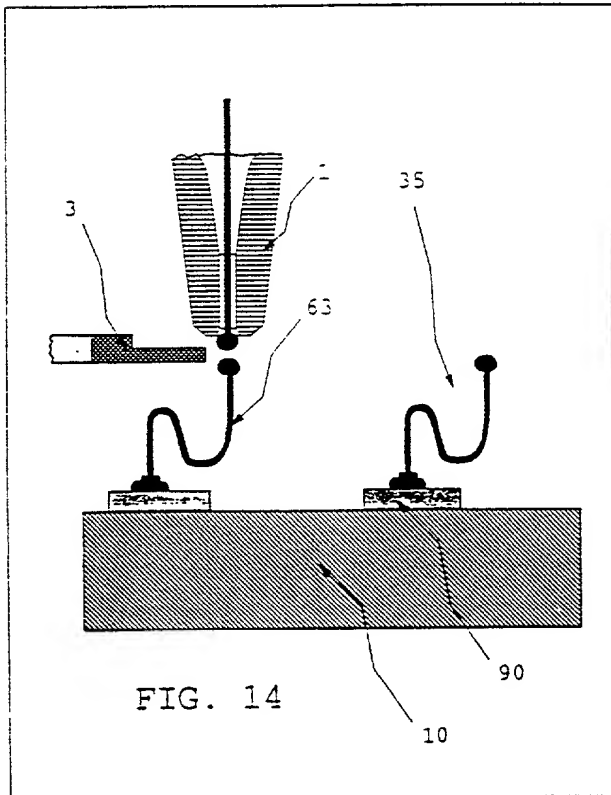
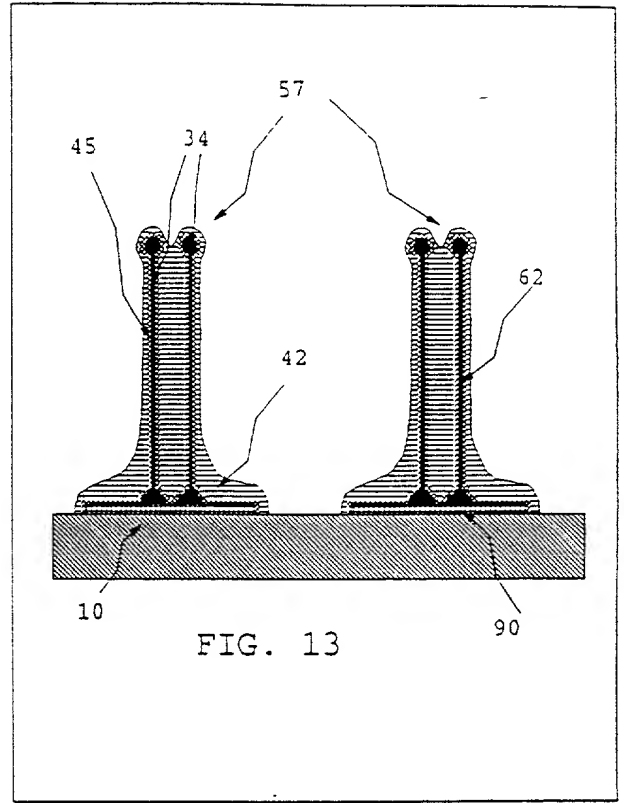
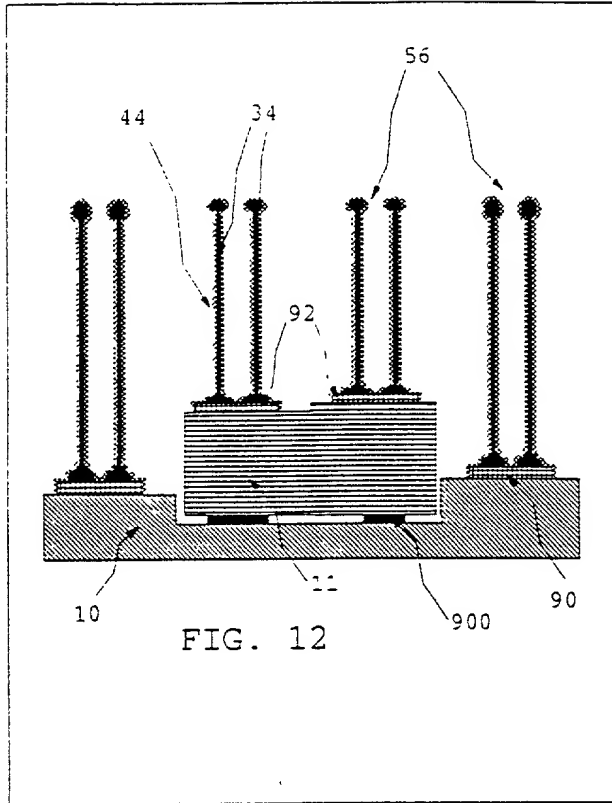
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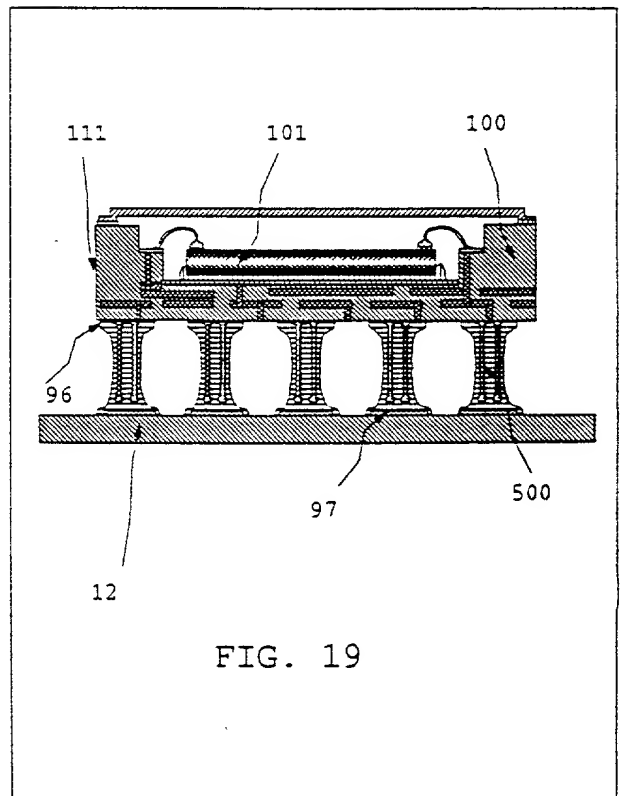
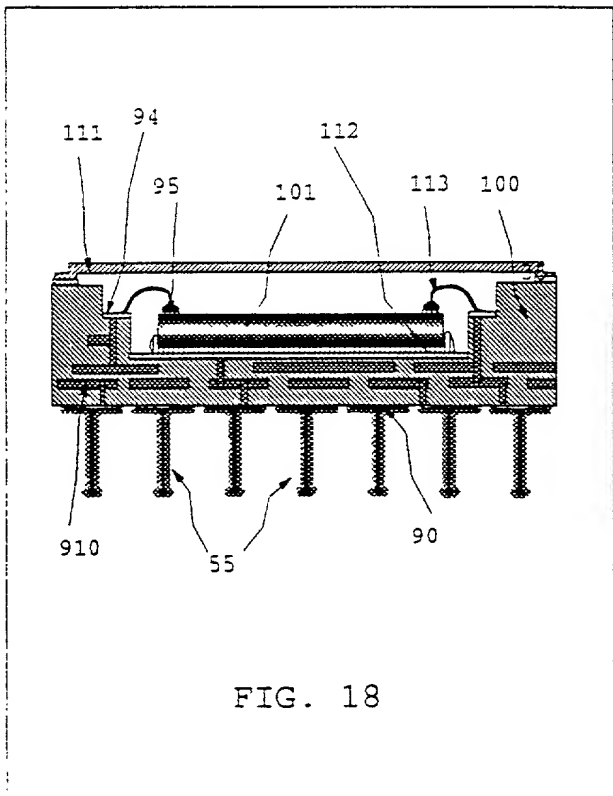
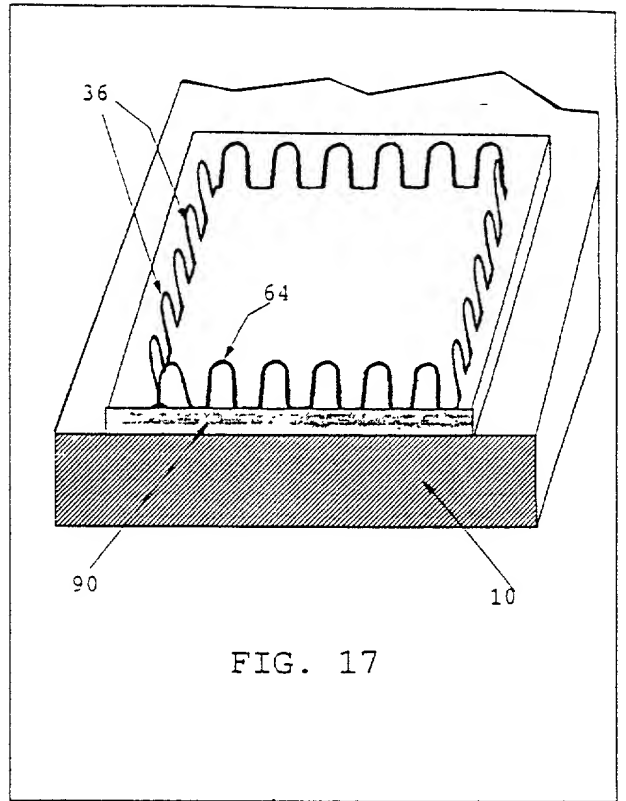
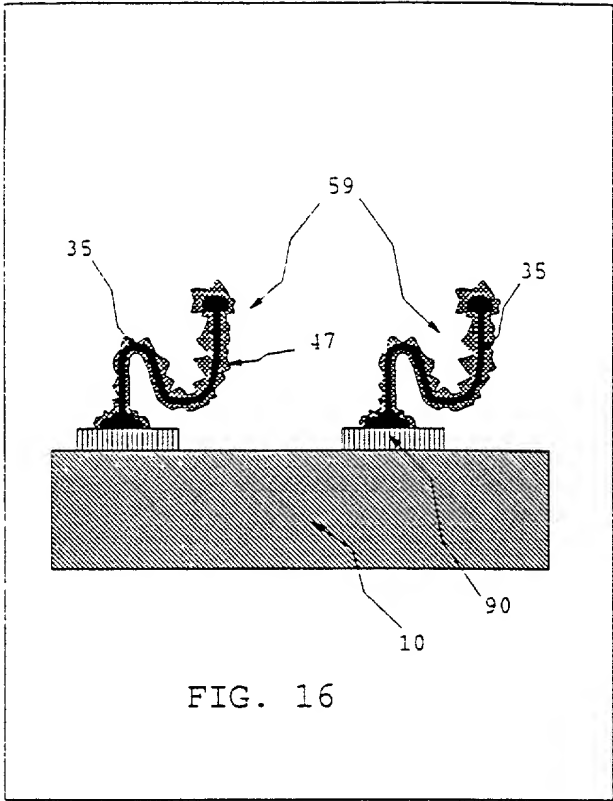


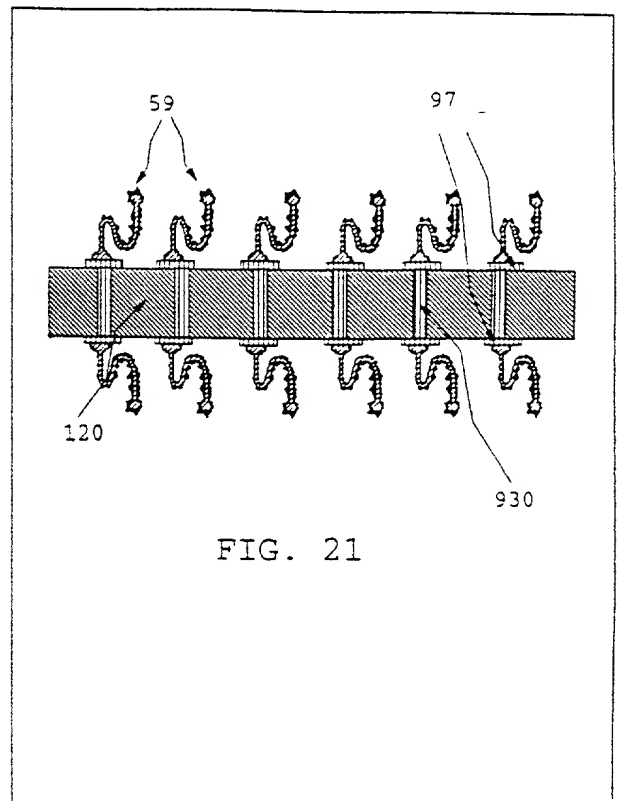
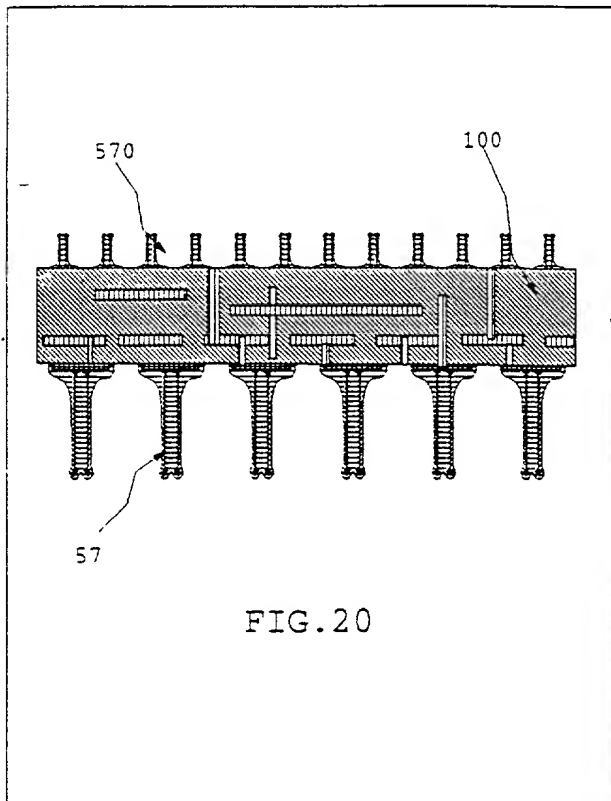












DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name; I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled METHOD FOR MANUFACTURING RAISED ELECTRICAL CONTACT PATTERN OF CONTROLLED GEOMETRY

the specification of which (check one): ☒ is attached hereto; ☐ was filed on _____ as Application Serial No. _____ and was amended on (or amended through) _____ (if applicable). I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment(s) referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a). I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

			Priority Claimed	
(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/> Yes	<input type="checkbox"/> No

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status — Patented, Pending or Abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY

① I (we) hereby appoint Charles E. Baxley, Registration No. 20,149, whose post office address is: Hart, Baxley, Daniels & Holton, 59 John Street, Fifth Floor, New York, New York 10038, telephone (212)791-7200, as my (our) attorney with full power of substitution and revocation, to prosecute this application, and to transact all business in the Patent and Trademark Office connected therewith.

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Date <u>November 12, 1993</u>	Signature <u>Igor Y. Khandros</u>

☐ See second page for additional joint inventors.